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NOTES:  
1.HSF Property;Comply iSupplier system HSF property attribute up-to-date value.

# CATALONIA

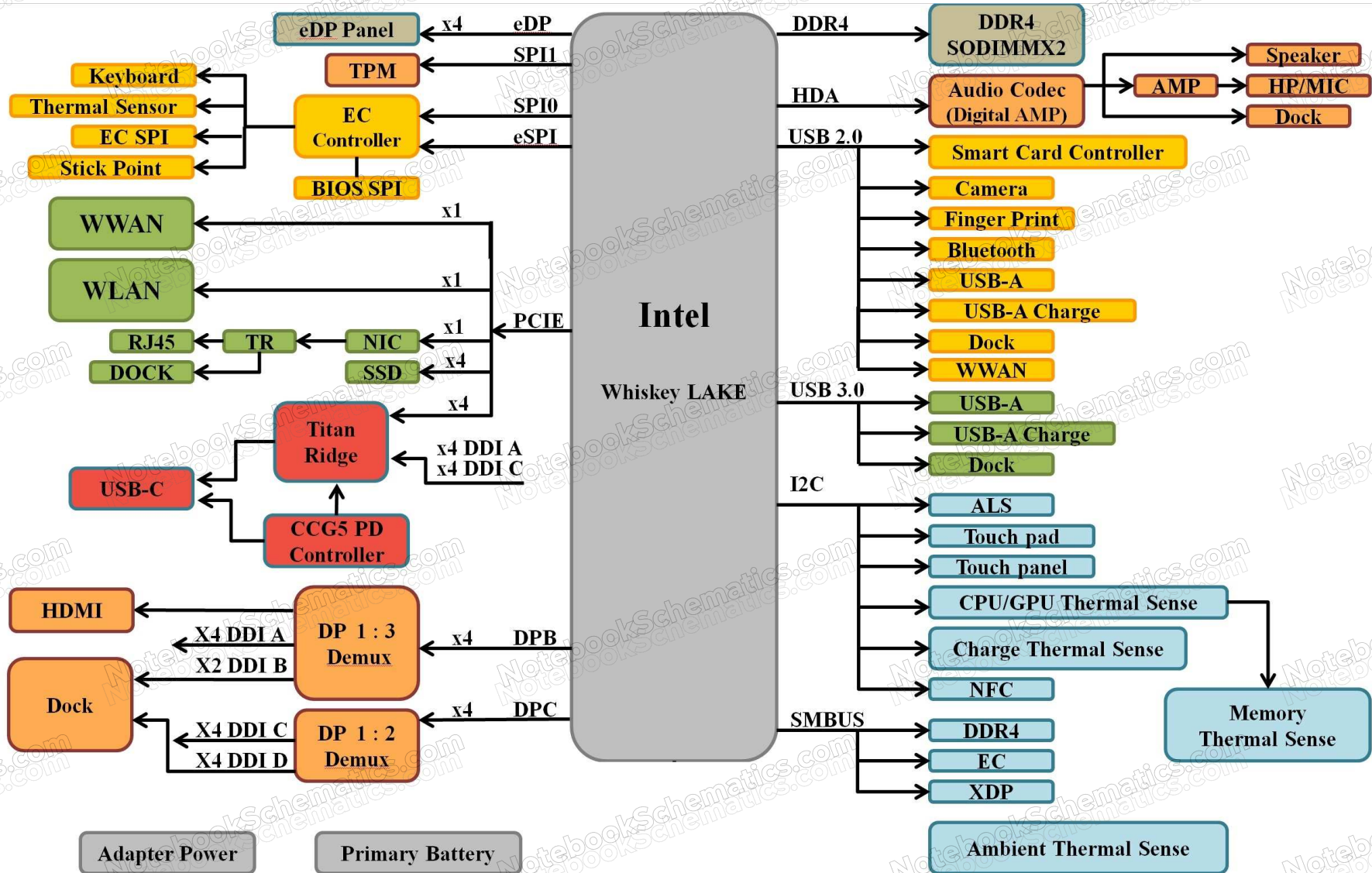
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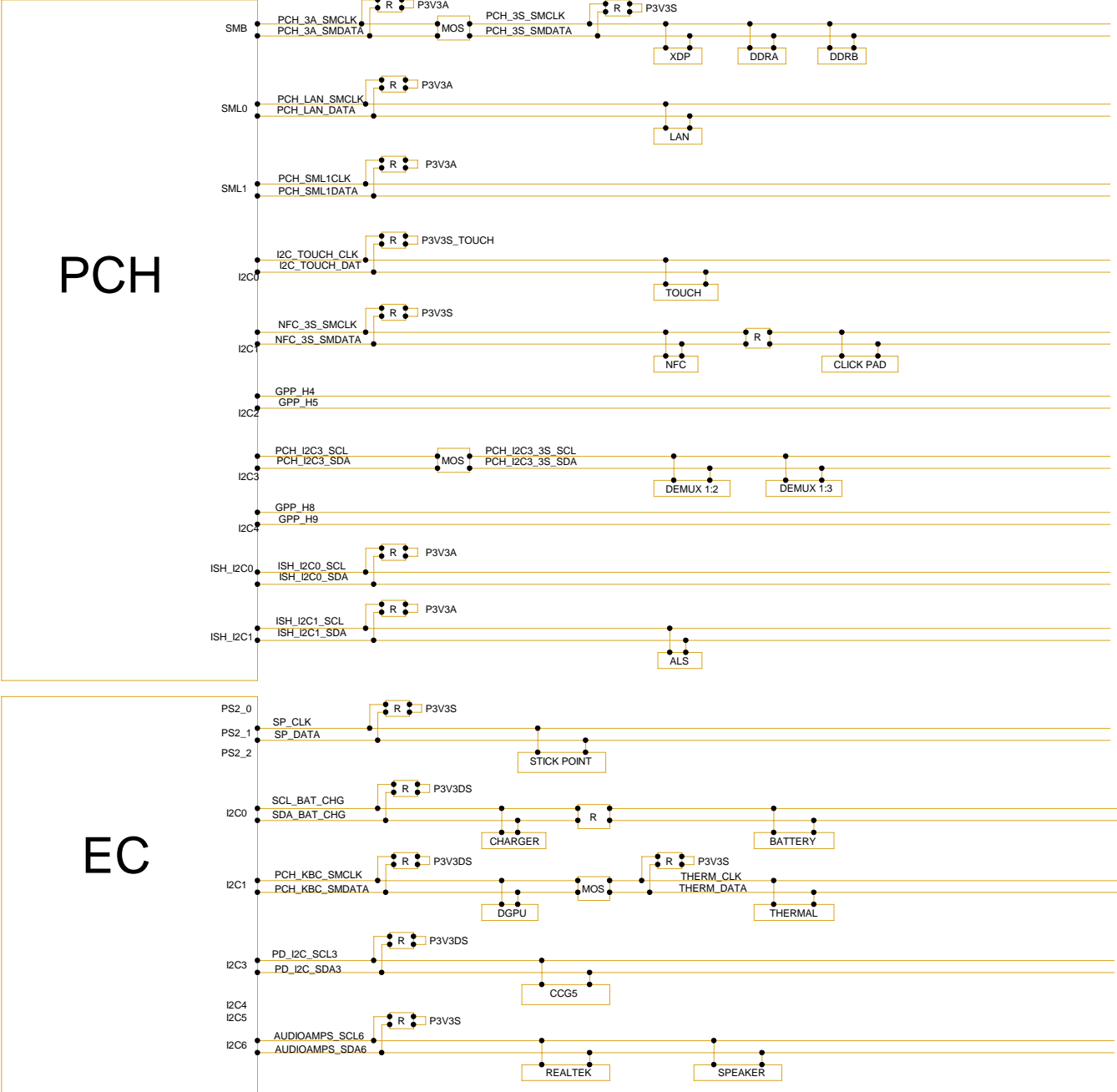
2019.03.27

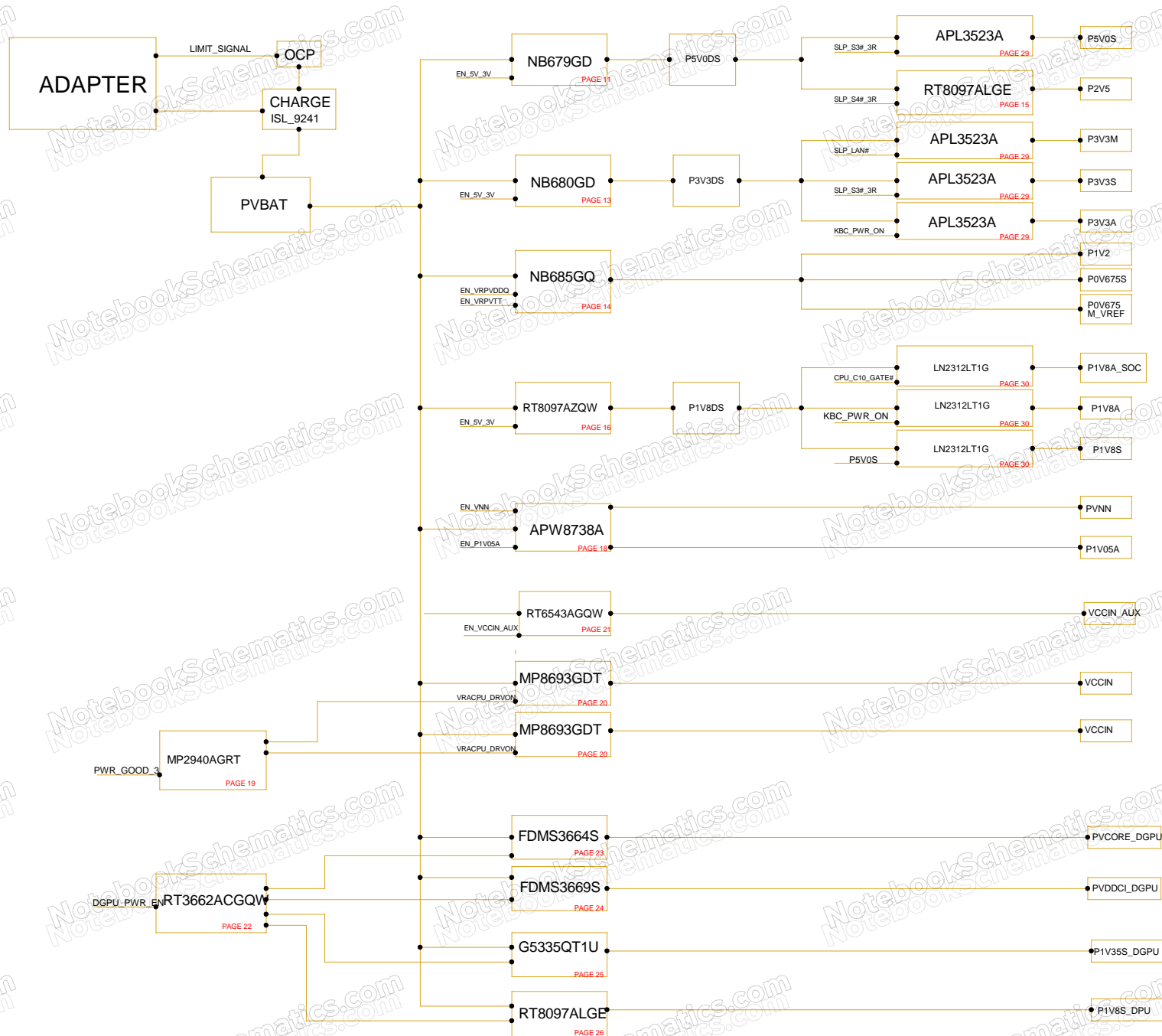
Marking	Description
I	Install
NI	Non-install
PROTO	Proto type
MP	Mass Production
TAA	Install for TAA
CRITICAL	Critical Parts

21-OCT-2002		
DATE	CHANGE NO.	REV

DESIGN / DRAWER				DATE			
CHECK				21-OCT-2002			
APPROVAL							
FILE NAME							
PCB P/N							







INVENTEC			
MODEL, PROJECT, FUNCTION	POWER BLOCK DIAGRAM	SIZE	CODE
CS	DOC NUMBER	1310XXXX-0-0	REV
SHEET	#	1	201

CHANGE IN	DATE	21-OCT-2002
PCB PIN	PCB VER	XXXX



A B C D E F

CB

A

A

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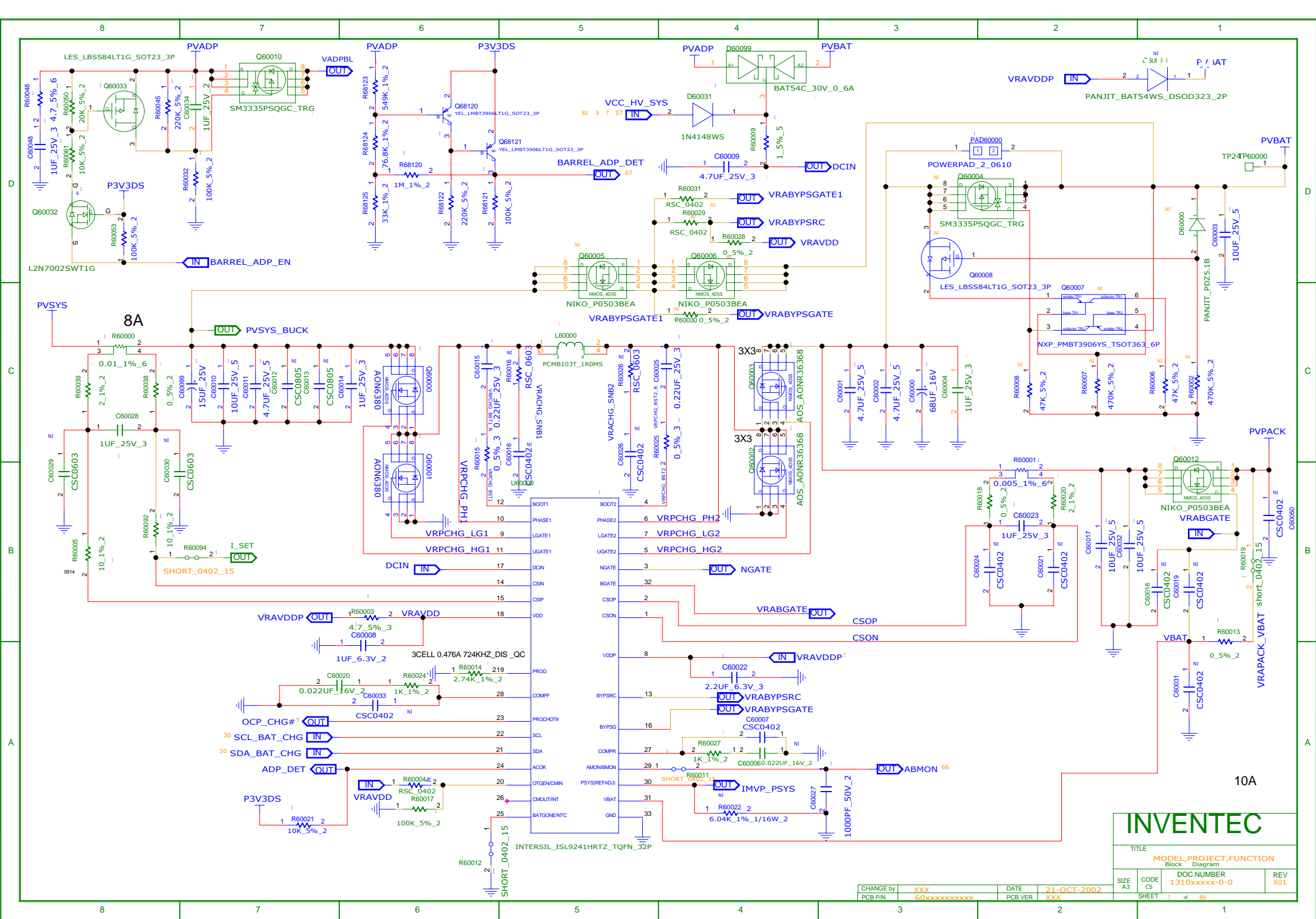
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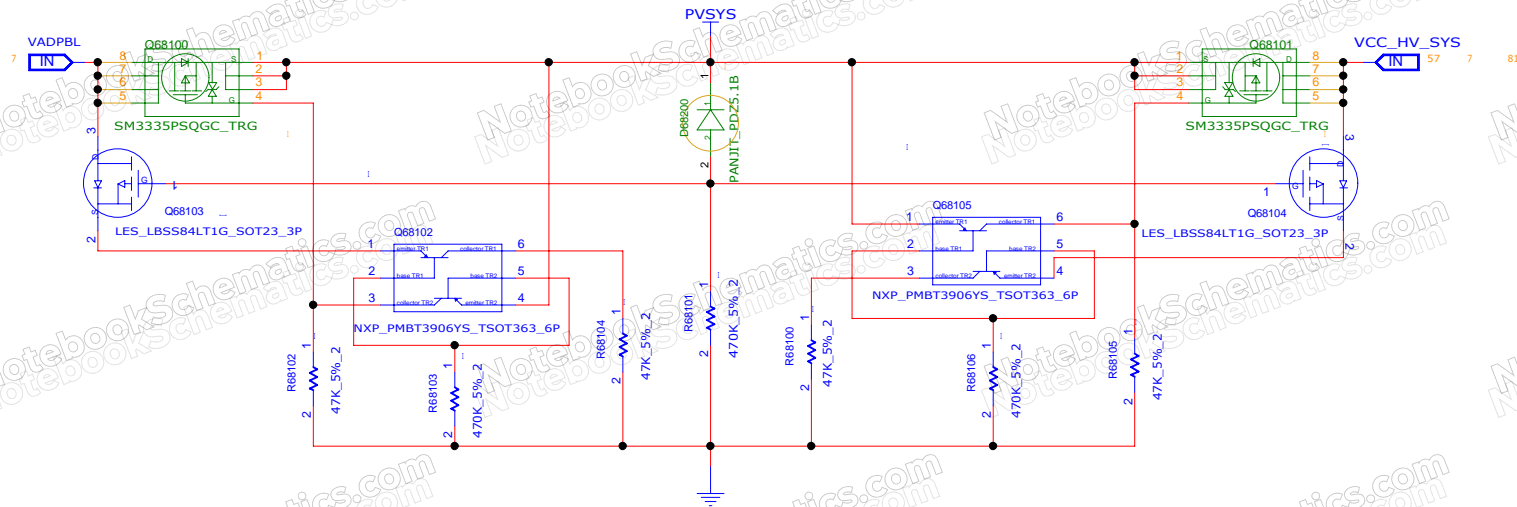
**INVENTEC**

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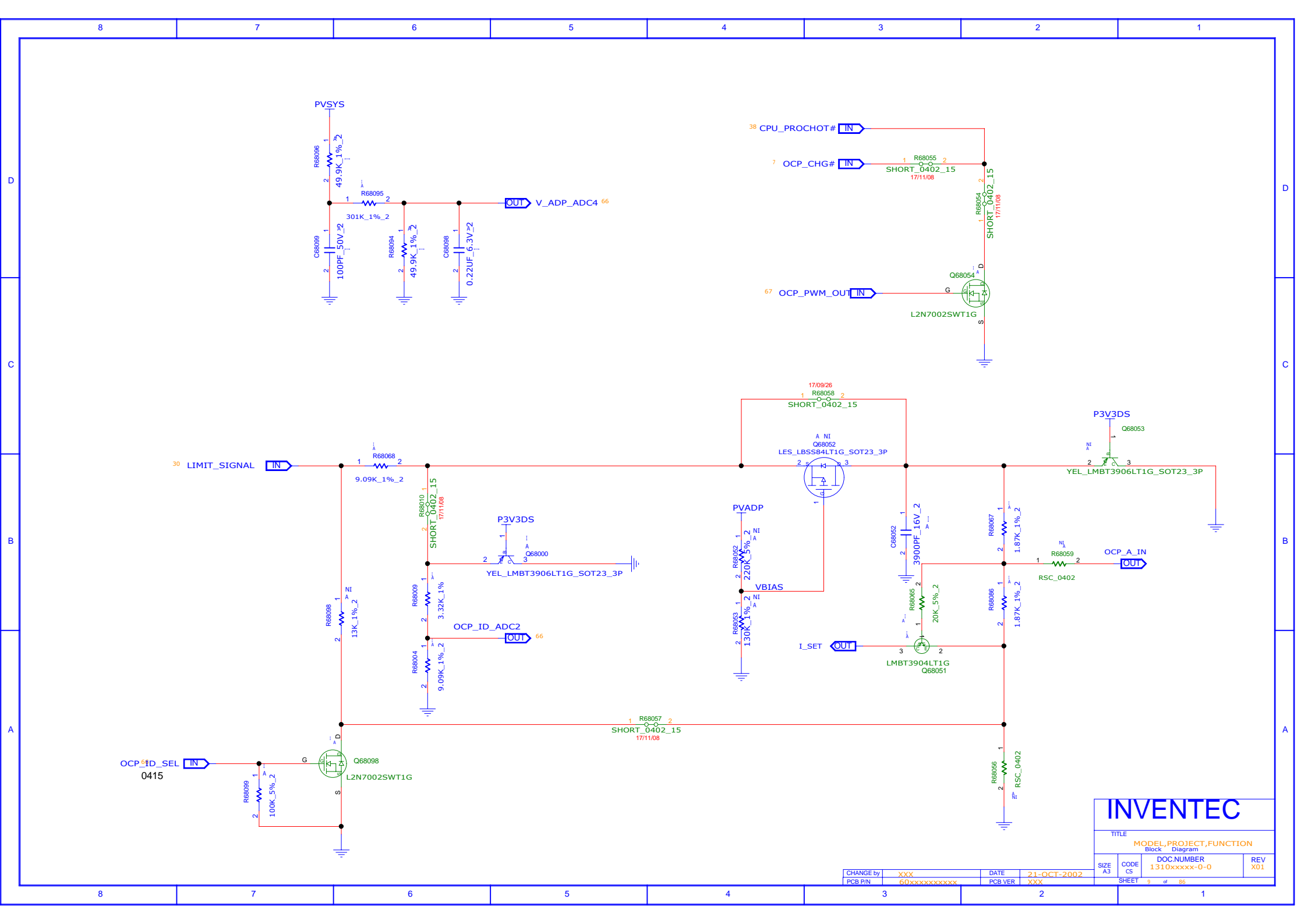
CHANGE by PCB P/N	XXX 60xxxxxxxxxxx	DATE PCB VER	21-OCT-2002 XXX
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MODEL, PROJECT, FUNCTION				
Block Diagram				
DOC NUMBER				
1310xxxx-0-0				
REV				
X01				
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A3	CS	21-OCT-2002	XXX	8 of 86

CHANGE by XXX  
PCB P/N 60xxxxxxxxxx



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

Block Diagram

SIZE A3

CODE CS

DOC NUMBER 1310xxxx-0-0

REV X01

CHANGE by PCB P/N

XXX 60xxxxxxxxxx

DATE PCB VER

21-OCT-2002 XXX

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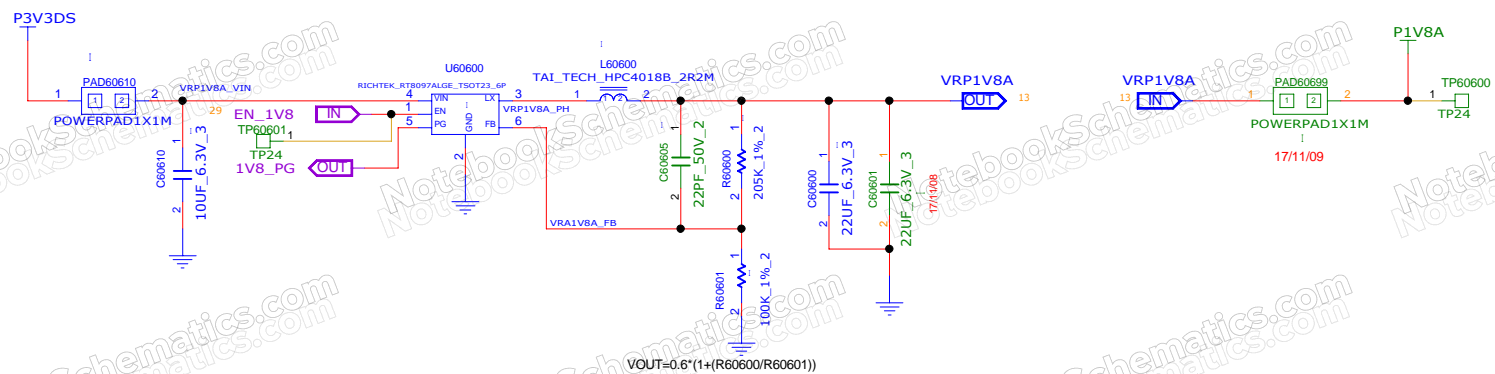








CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX



INVENTEC

TITLE  
MODEL, PROJECT, FUNCTION  
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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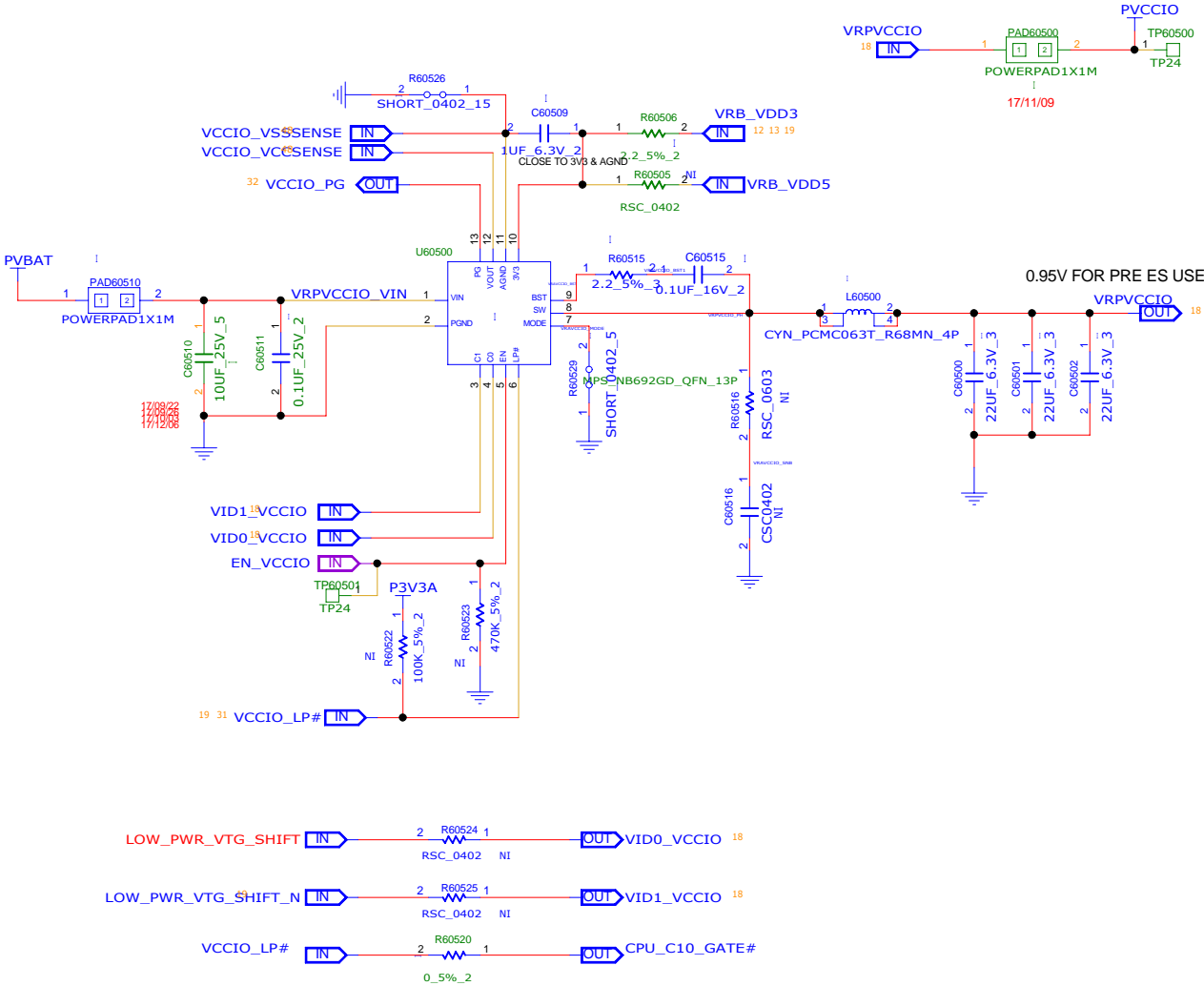
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MODE	VR Rail	Resistor to GND (1% Accuracy)
M1	VCCIO	0
M2	PRIMCORE	Float or > 230K
M3	EDRAM/V1.0A/EOPIO	100K
M4	Others	150K

	LP#	C1	C0	VOUT(V)
VCCIO	0	X	X	0
	1	0	0	0.85
	1	0	1	0.875
	1	1	0	0.95
	1	1	1	0.975
VCCPRIM _CORE	0	X	X	0.75
	1	0	0	0.9
	1	0	1	0.95
	1	1	0	1.0
	1	1	1	1.05
EDRAM/ EOPIO/ V1.0A	0	X	X	0
	1	0	0	0.8 (MSM)
	1	0	1	0.95
	1	1	0	1
	1	1	1	1.05
Others (Fixed design only, not allowed for changing on-the-fly)	0	0	0	1.59
	0	0	1	1.99
	0	1	0	2.38
	0	1	1	3.3
	1	0	0	1.2
	1	0	1	1.5
	1	1	0	1.8
	1	1	1	2.5



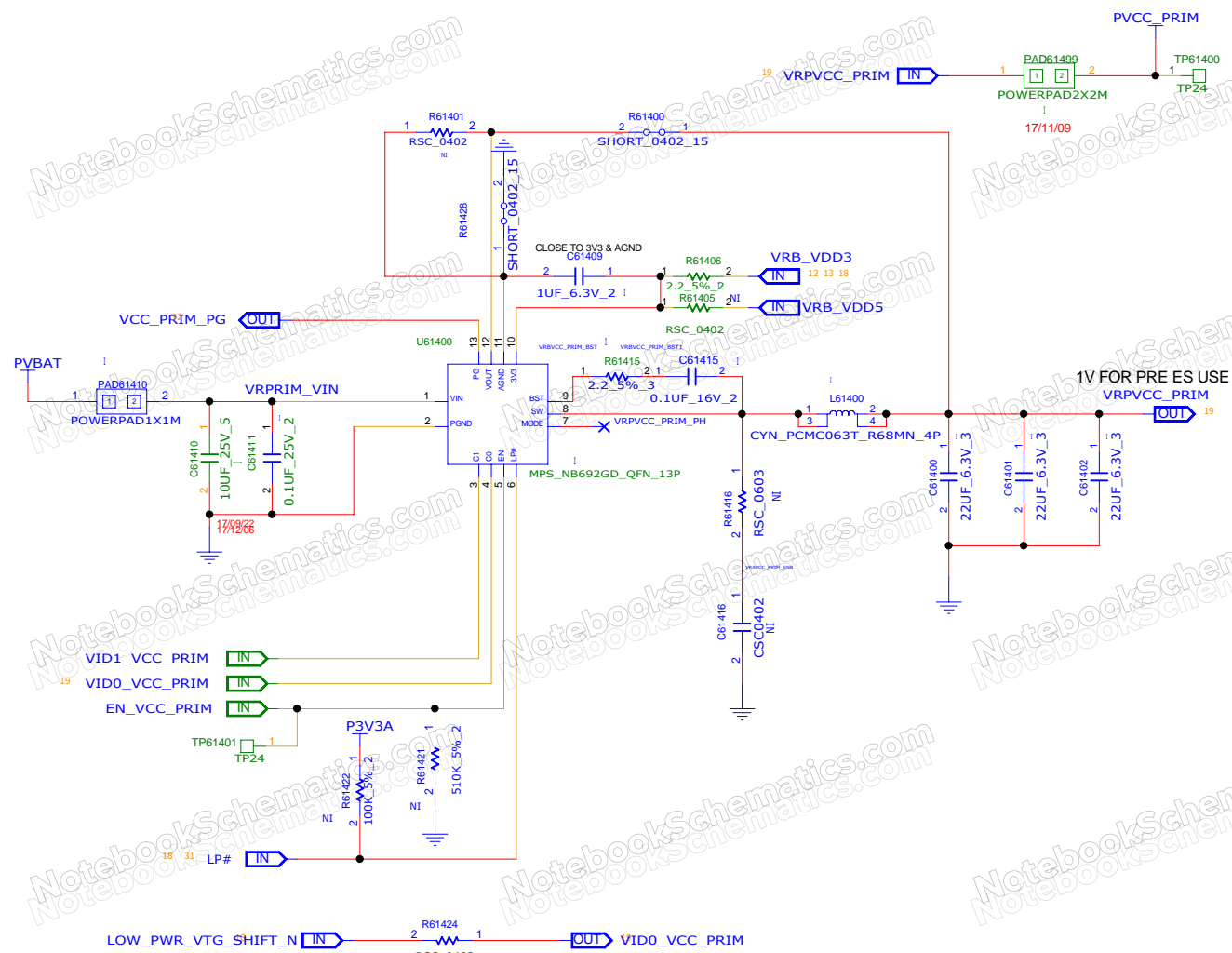
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MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
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CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxxx	PCB VER	XXX

MODE	VR Rail	Resistor to GND (1% Accuracy)
M1	VCCIO	0
M2	PRIMCORE	Float or > 230K
M3	EDRAM/V1.0A/EOPIO	100K
M4	Others	150K

	LP#	C1	C0	VOUT(V)
<b>VCCIO</b>	0	X	X	0
	1	0	0	0.85
	1	0	1	0.875
	1	1	0	0.95
	1	1	1	0.975
<b>VCCPRIM _CORE</b>	0	X	X	0.75
	1	0	0	0.9
	1	0	1	0.95
	1	1	0	1.0
	1	1	1	1.05
<b>EDRAM/ EOPIO/ V1.0A</b>	0	X	X	0
	1	0	0	0.8 (MSM)
	1	0	1	0.95
	1	1	0	1
	1	1	1	1.05
<b>Others</b> (Fixed design only, not allowed for changing on-the-fly)	0	0	0	1.59
	0	0	1	1.99
	0	1	0	2.38
	0	1	1	3.3
	1	0	0	1.2
	1	0	1	1.5
	1	1	0	1.8
	1	1	1	2.5



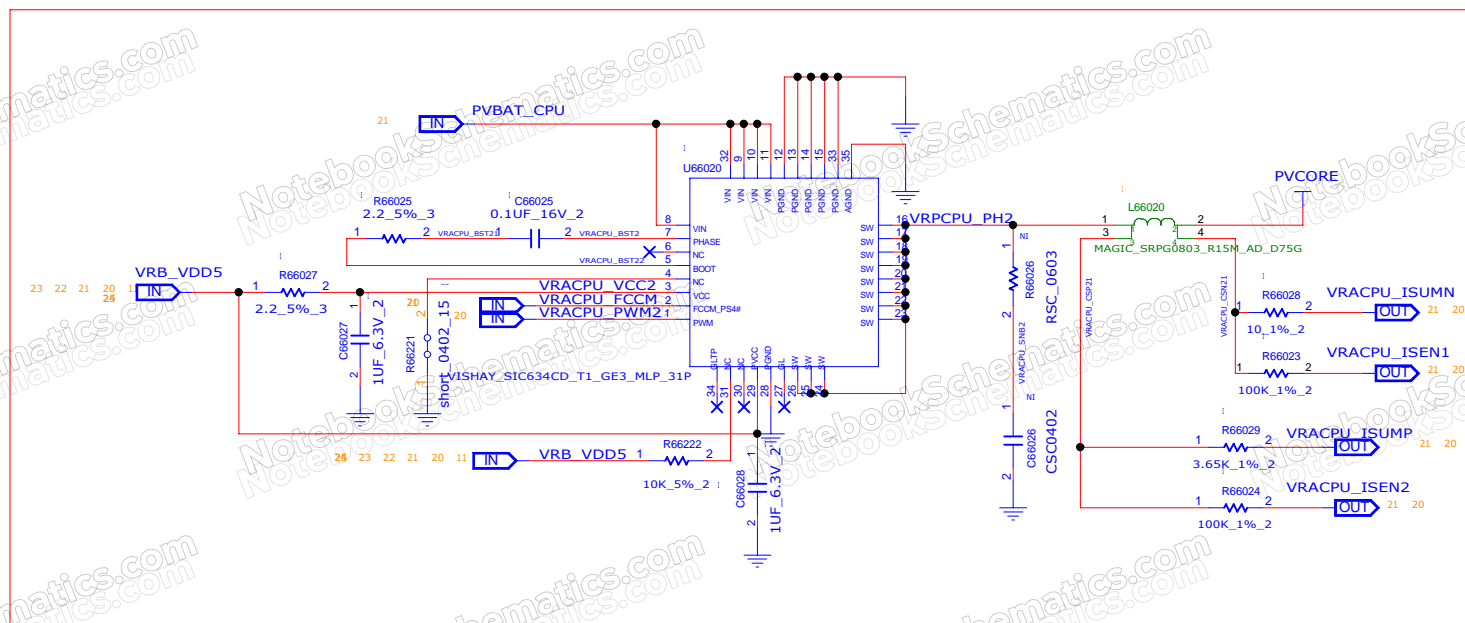
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		Block	Diagram
SIZE A3	CODE CS	DOC. NUMBER 1310xxxxxx-0-0	
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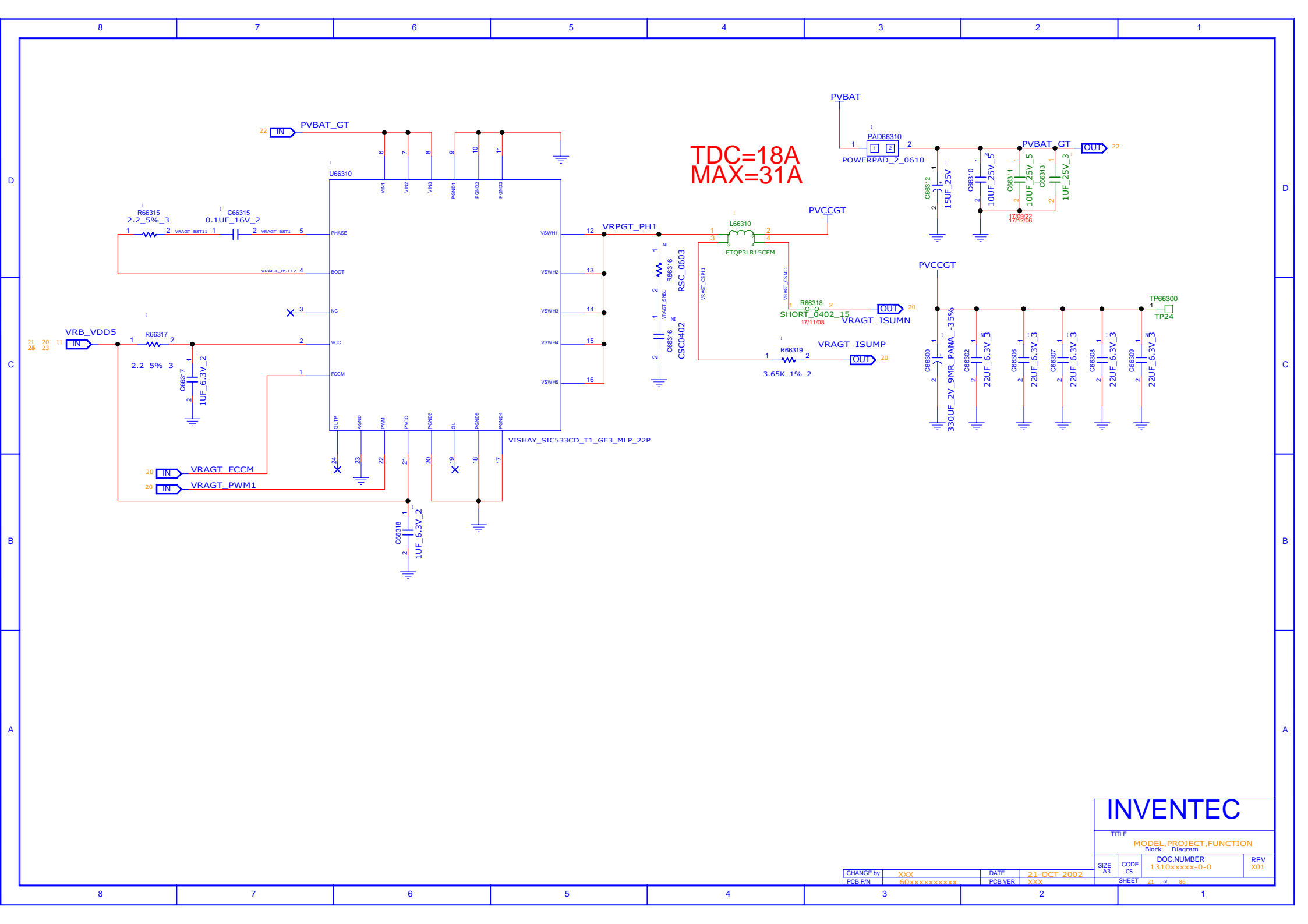


TDC=48A  
MAX=70A



SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	F X
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CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX



TDC=18A  
MAX=31A

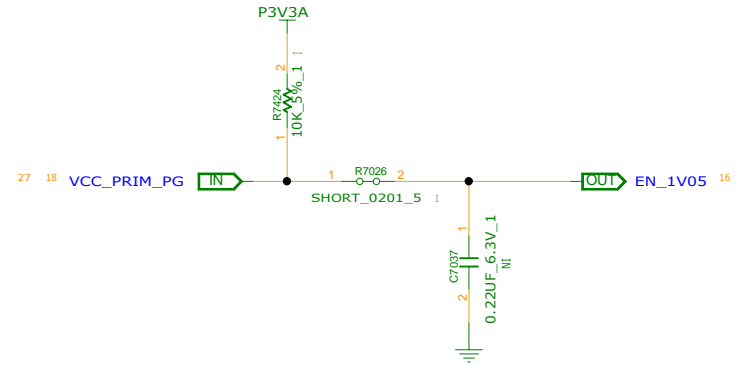
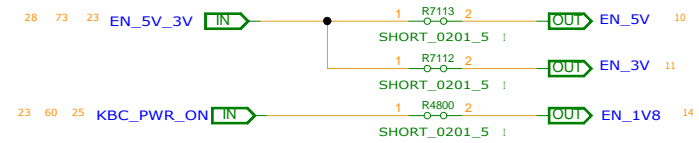
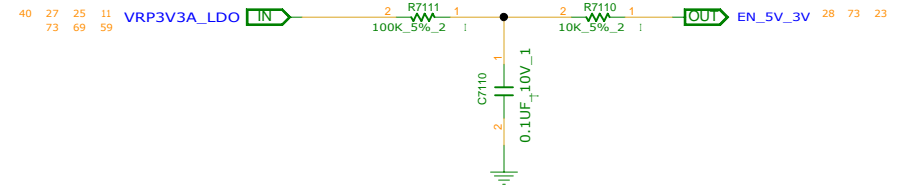
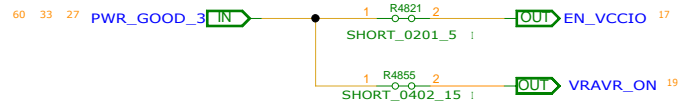
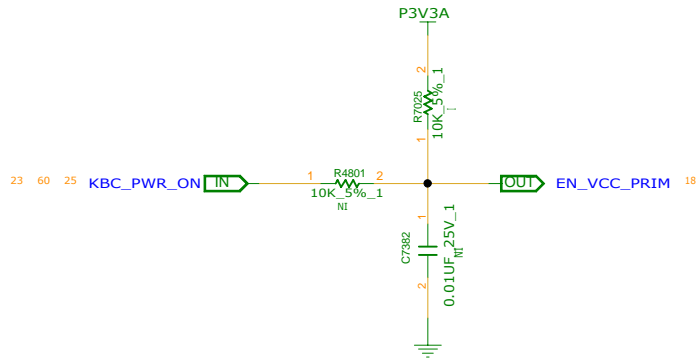
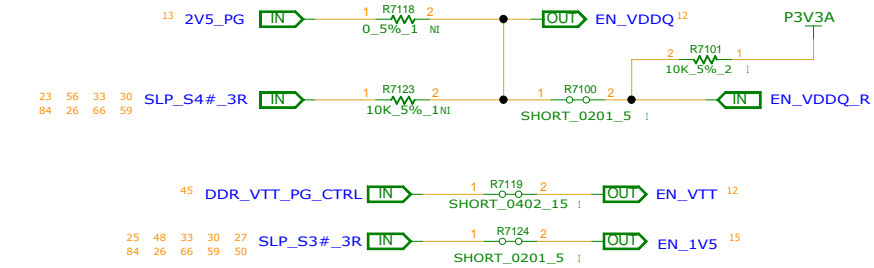
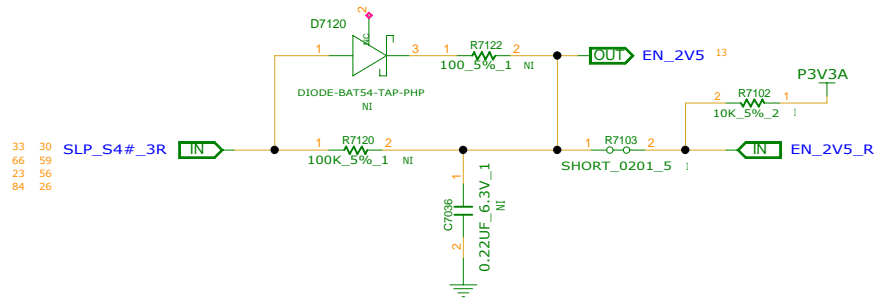
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TITLE  
MODEL,PROJECT,FUNCTION  
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 21 of 86			

CHANGE by PCB P/N	XXX 60xxxxxxxxxxx	DATE PCB VER	21-OCT-2002 XXX
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# POWER ENABLE SIGNAL

## POWER TO EE NET NAME CONNECTION

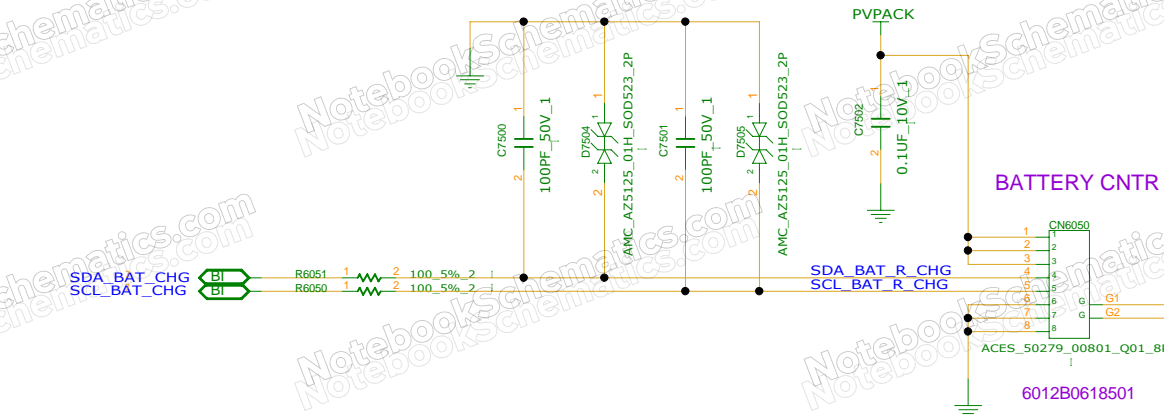
LOCATION NUMER : 7000~7499

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

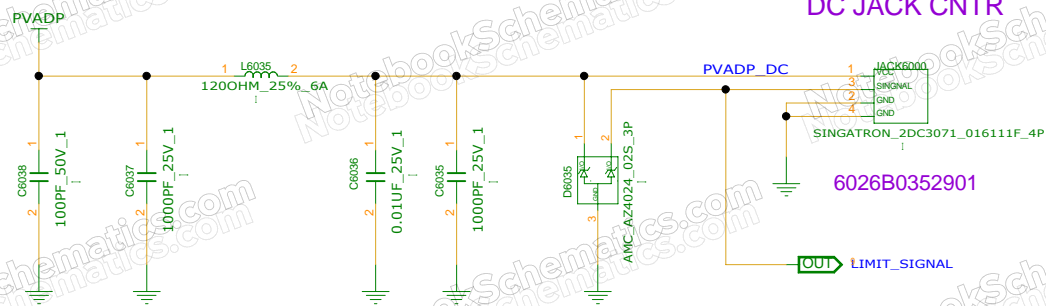
INVENTEC			
TITLE POWER ENABLE SIGNAL CONNECTION			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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LOCATION NUMER : 6050~6099

LOCATION NUMER : 6000~6049



DC JACK CNTR



INVENTEC

TITLE

MODEM AC RPT. CNTR. I/O

SIZE A3

CODE CS

DOC NUMBER

1310xxxx-0-0

REV

X01

CHANGE by XXX

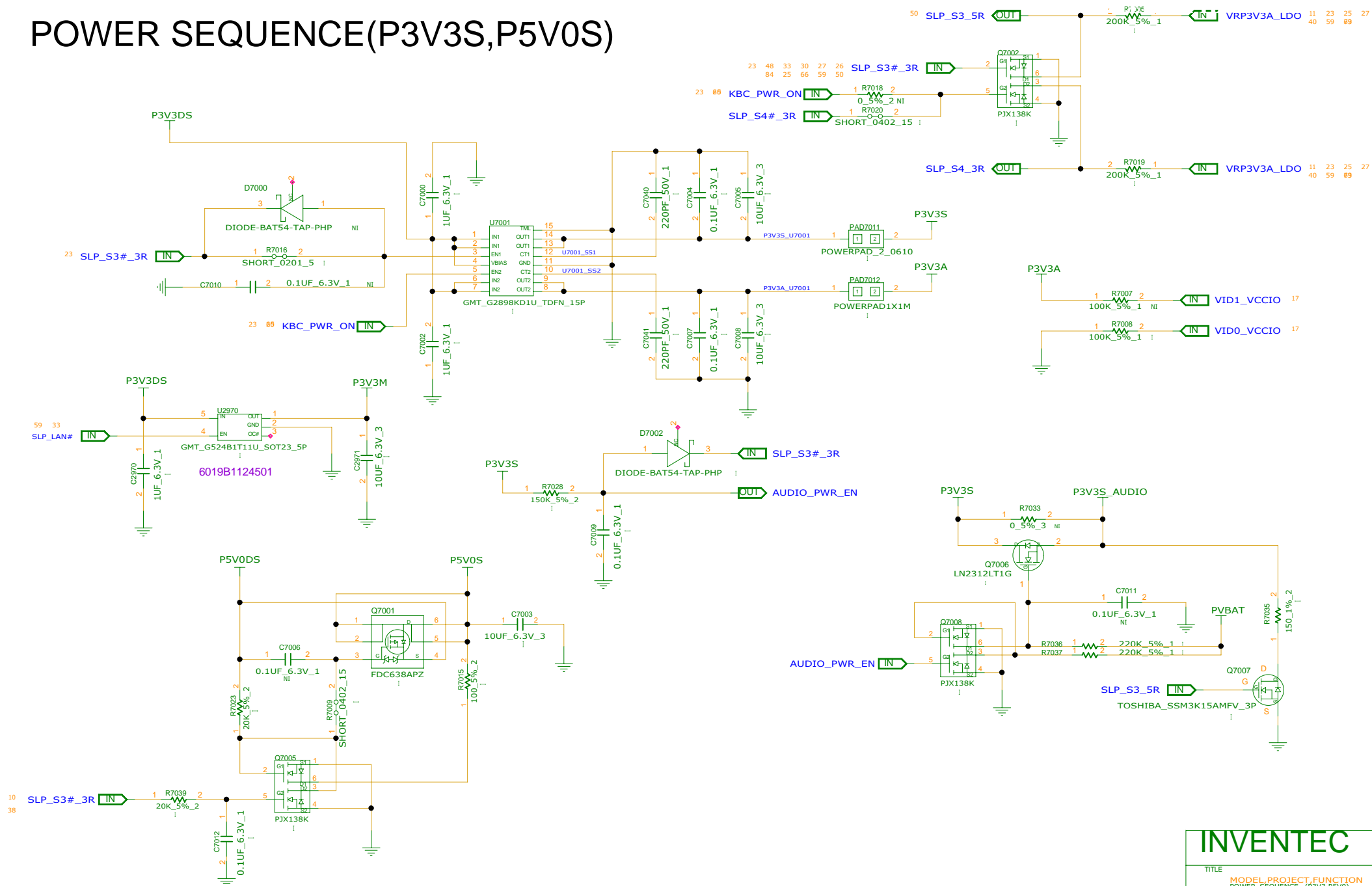
PCB P/N 60xxxxxxx

DATE 21-OCT-2002

PCB VER XXX

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## POWER SEQUENCE(P3V3S,P5V0S)

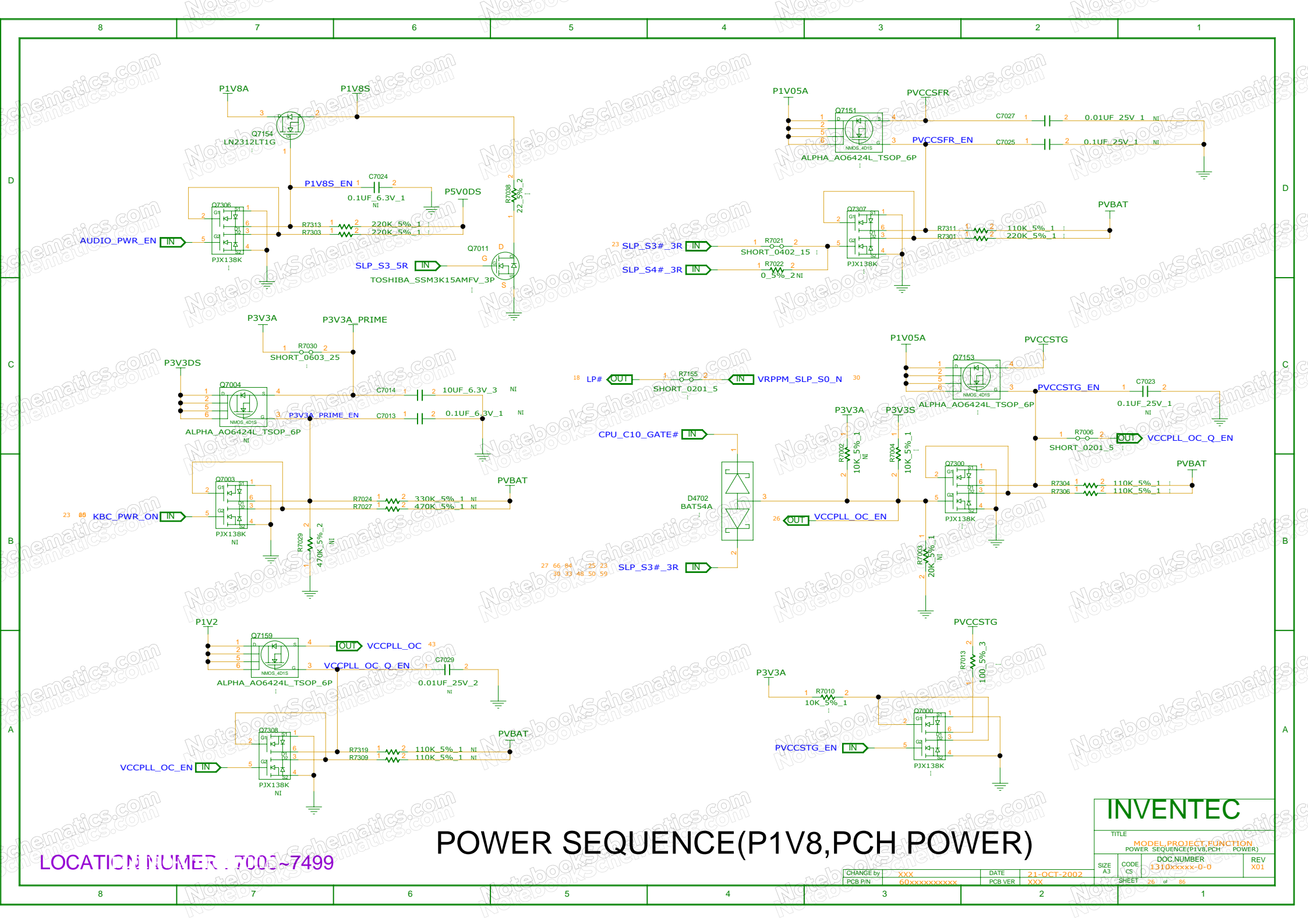


INVENTEC

TITLE				MODEL,PROJECT,FUNCTION			
				POWER SEQUENCE (P3V3,P5V0)			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0			RE X0		
SHEET		25		of		86	

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxxx	PCB VER	XXX



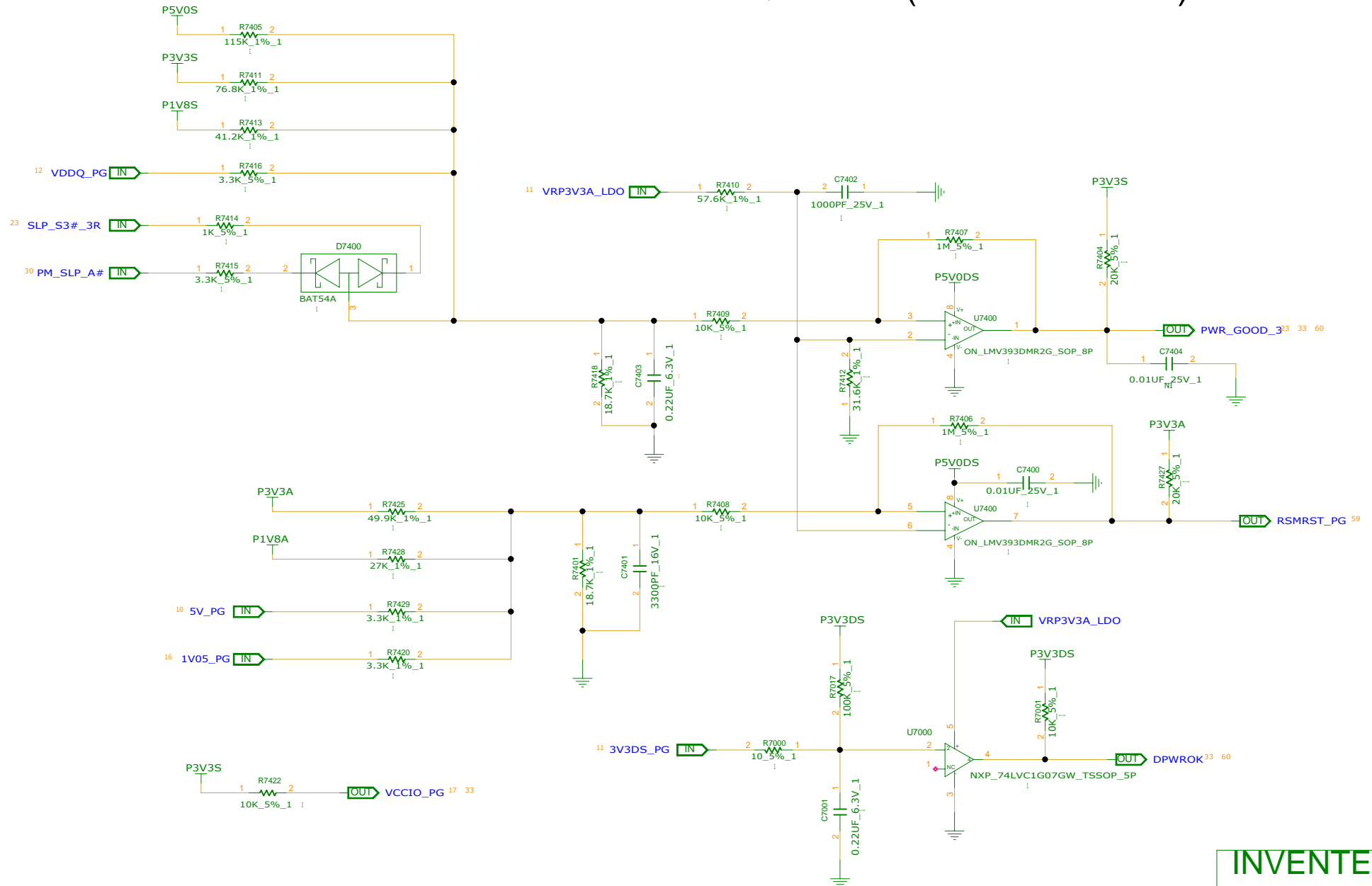


LOCATION NUMBER : 7000~7499

# POWER SEQUENCE(P1V8,PCH POWER)

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
POWER SEQUENCE(P1V8,PCH POWER)			
DOC NUMBER			
1310xxxx-0-0			
REV			
X01			
SIZE	CODE	DATE	21-OCT-2002
A3	CS	PCB VER	XXX
SHEET	26	of	86

# POWER SEQUENCE(PG & RSMRST)



LOCATION NUMBER : 7000~7499

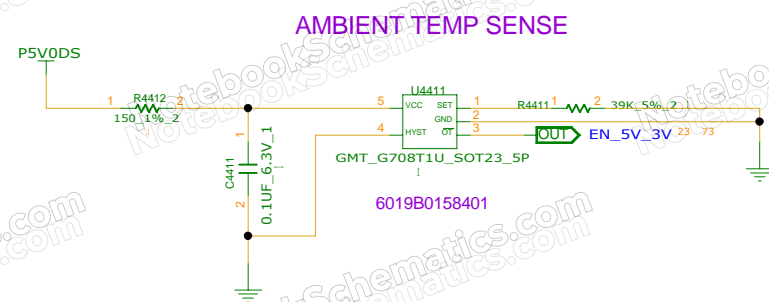
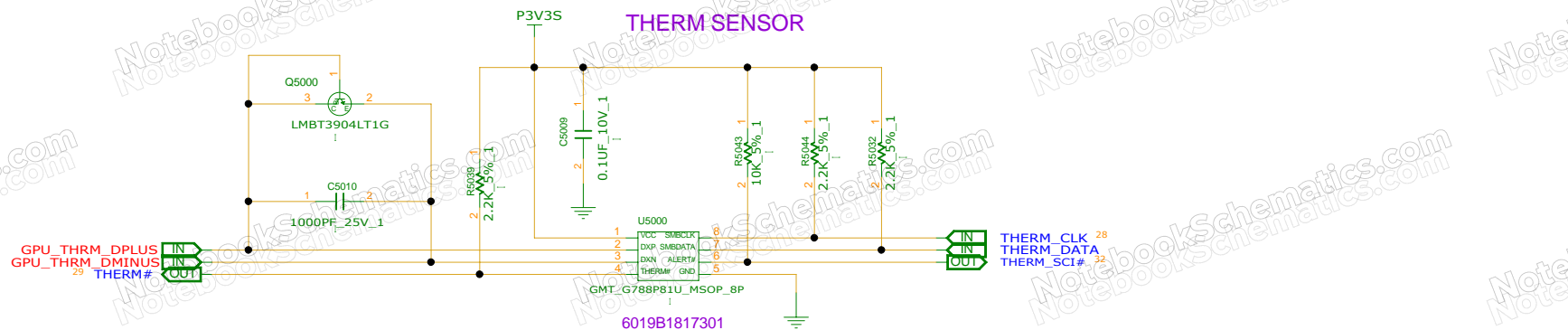
INVENTEC

TITLE			
MODEL PROJECT FUNCTION POWER SEQUENCE(PG & RSMRST)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 27 of 86			

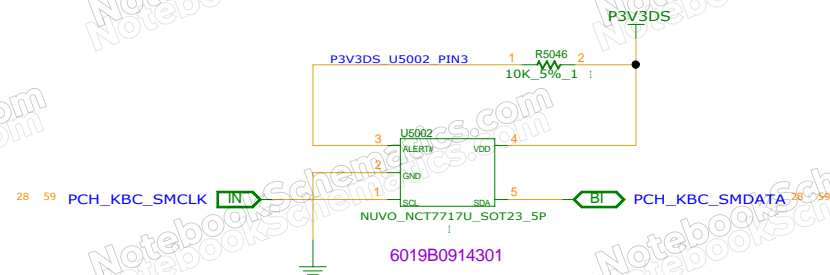
CHANGE by PCB P/N	XXX 60xxxxxxxxxx	DATE PCB VER	21-OCT-2002 XXX
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# THERMAL

REFERENCE NUMBER:4400~5400



$RSET (K OHM) = 0.0012T^2 - 0.9308T + 96.147$   
THERMAL SHUTDOWN AT 68 DEG.



LOCATION NUMBER:4401~4449

## INVENTEC

TITLE  
MOTHERBOARD PROJECT,FUNCTION

SIZE A3  
CODE CS  
SHEET 28 of 86

DOC NUMBER  
1310xxxx-0-0

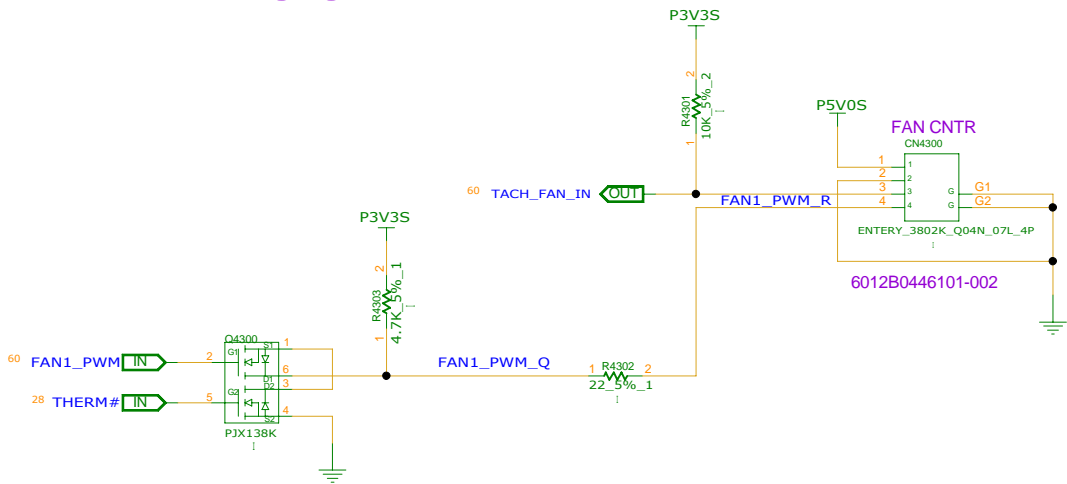
REV  
X01

CHANGE by  
PCB P/N

DATE  
PCB VER

21-OCT-2002  
XXX

# FAN CONN



LOCATION NUMBER.4300~4349

INVENTEC

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxxx	PCB VER	XXX

TITLE			
MODEL PROJECT,FUNCTION			
FAN_CNTR			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
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# CPU XDP CONNECTOR

D

C

B

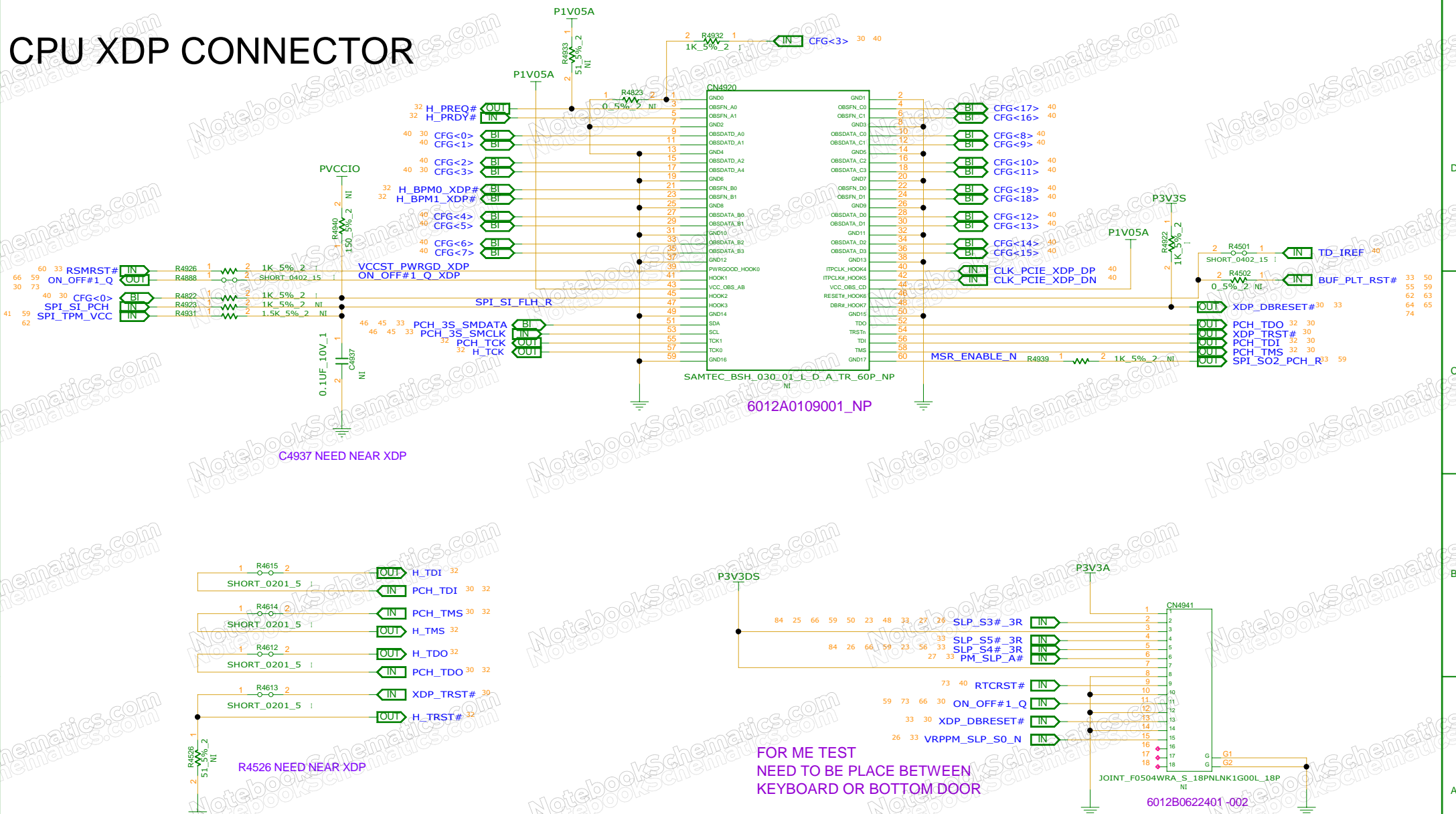
A

D

C

B

A



LOCATION NUMBER: 4500-4399

INVENTEC

TITLE  
MCU BFL & PRO CONN. FUNCTION

SIZE  
A3

CODE  
CS

DOC NUMBER  
1310xxxx-0-0

REV  
X01

CHANGE by  
PCB P/N

DATE  
21-OCT-2002

PCB VER  
XXX

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LOCATION NUMBER:400-449

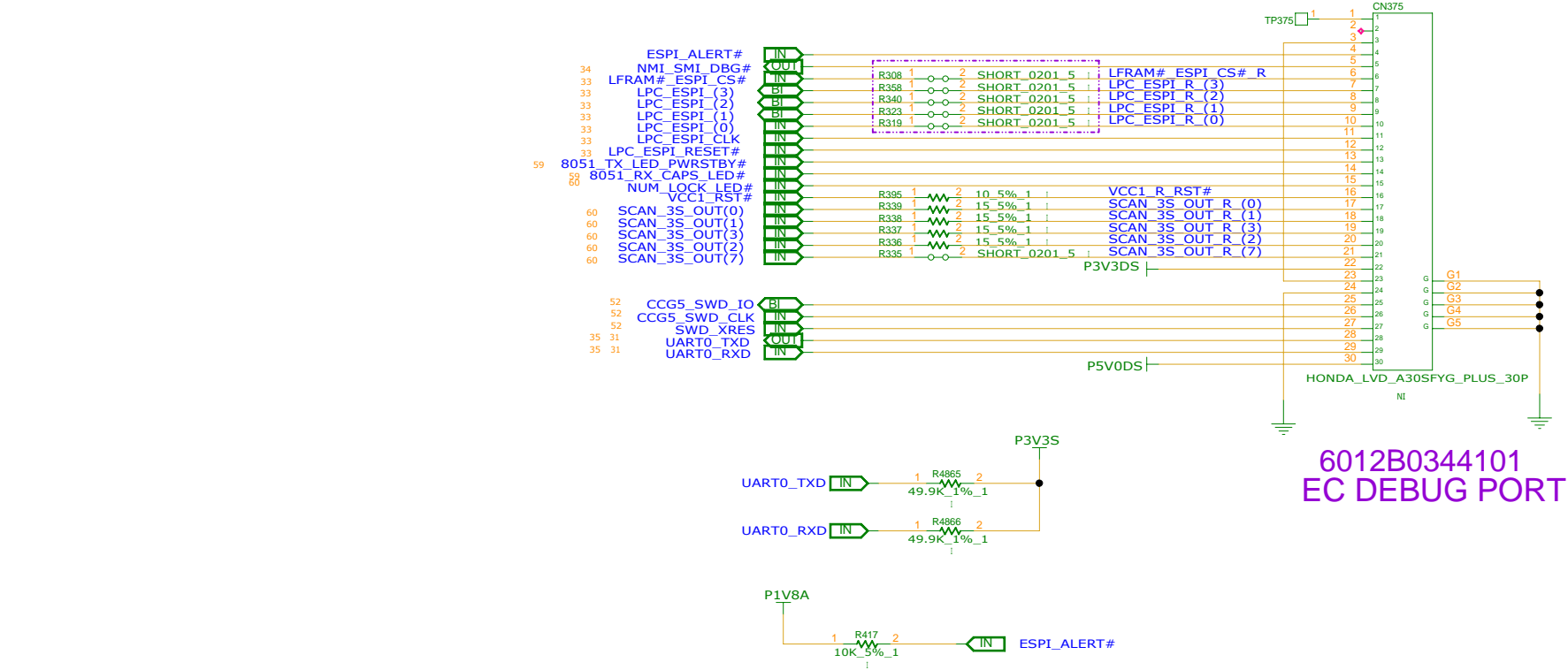
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PCB P/N 60xxxxxxxxxx

DATE 21-OCT-2002  
PCB VER XXX

INVENTEC

TITLE MC9800 PROJECT,FUNCTION

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 31 of 86			



D

C

B

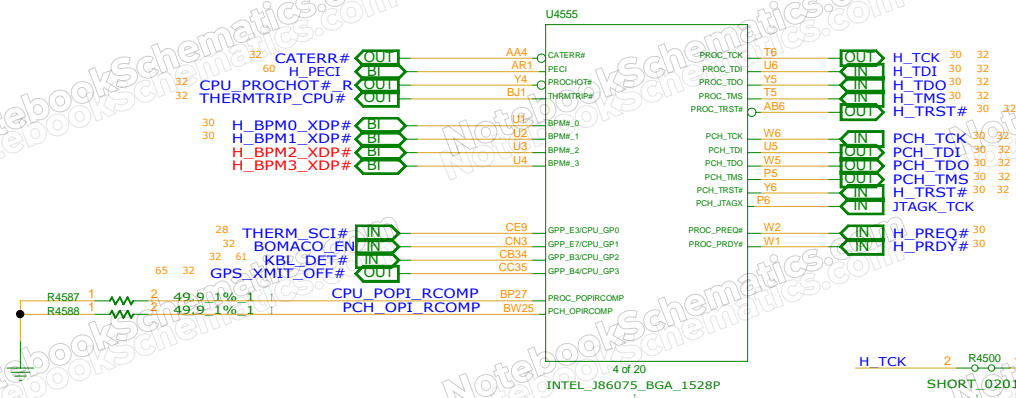
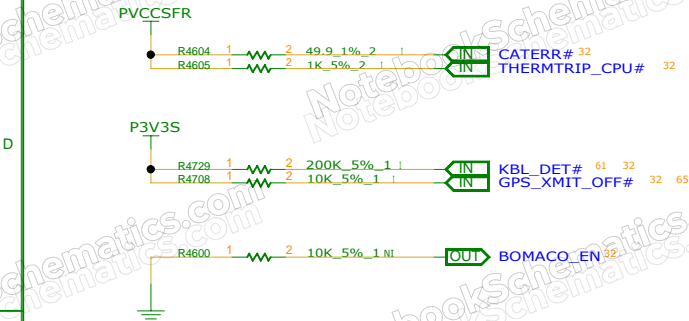
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D

C

B

A

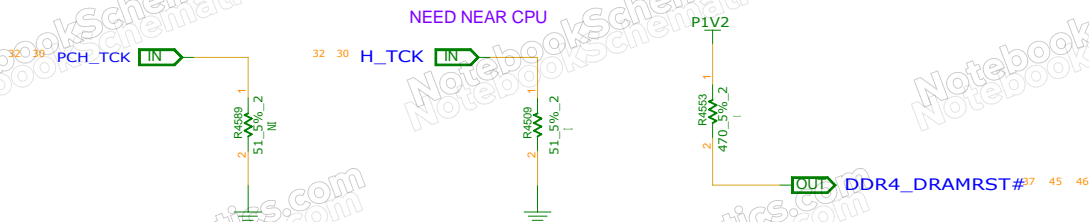
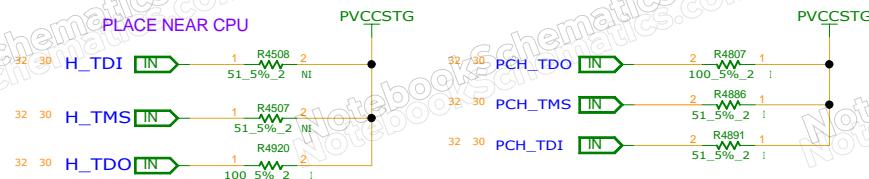
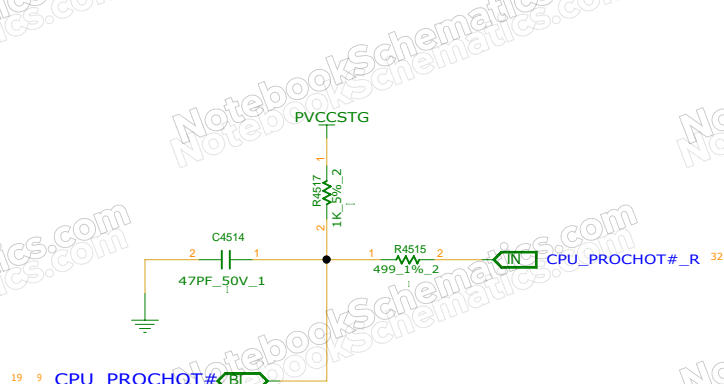


LAYOUT NOTE: PLACE R4808 NEAR PCH

LAYOUT NOTE: JTAG\_TMS TERMINATIONS NEED TO BE PLACED NEAR PCH

LAYOUT NOTE: JTAG\_TDI TERMINATIONS NEED TO BE PLACED NEAR PCH

LAYOUT NOTE: JTAG\_TDO TERMINATIONS NEED TO BE PLACED NEAR XDP



LOCATION NUMBER: 4500-4999

INVENTEC

TITLE

MODEM PROJECT FUNCTION

SIZE A3

CODE CS

DOC NUMBER 1310xxxx-0-0

REV X01

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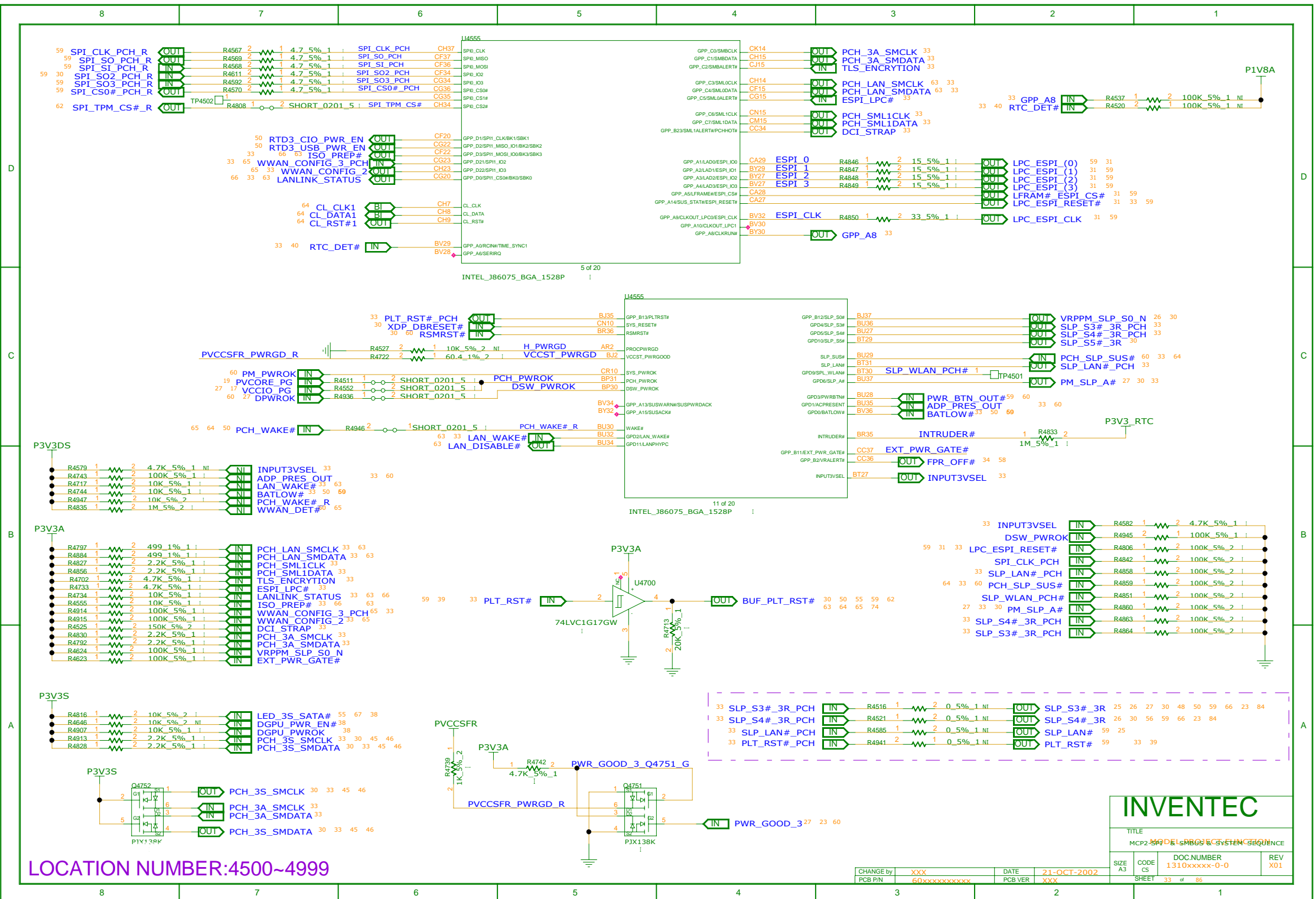
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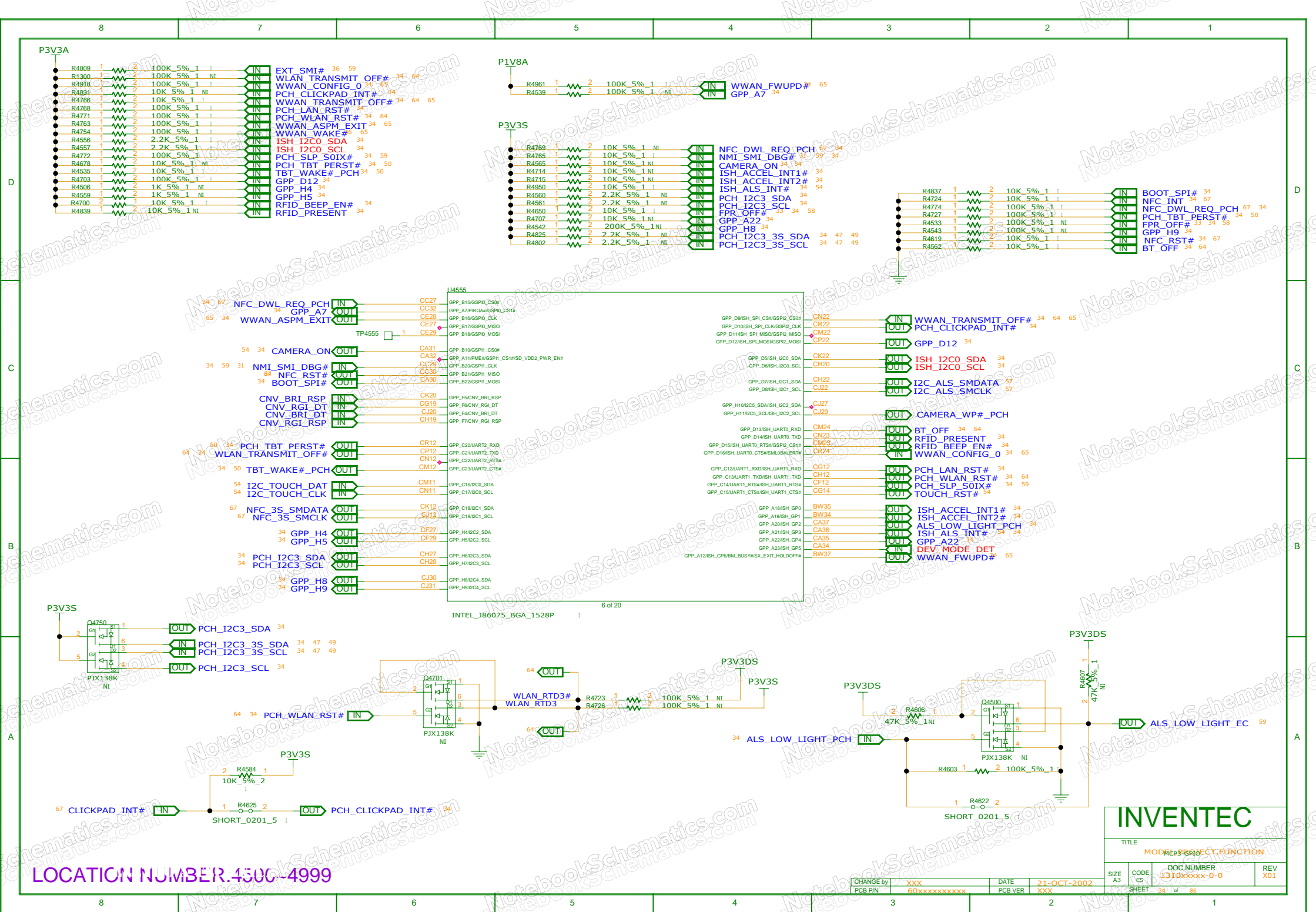
DATE 21-OCT-2002

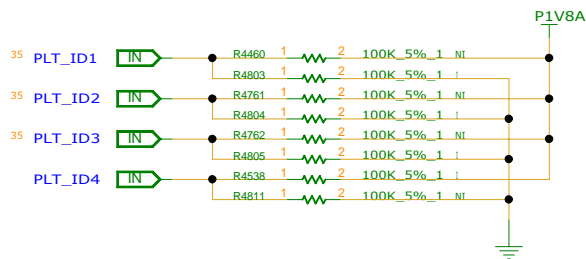
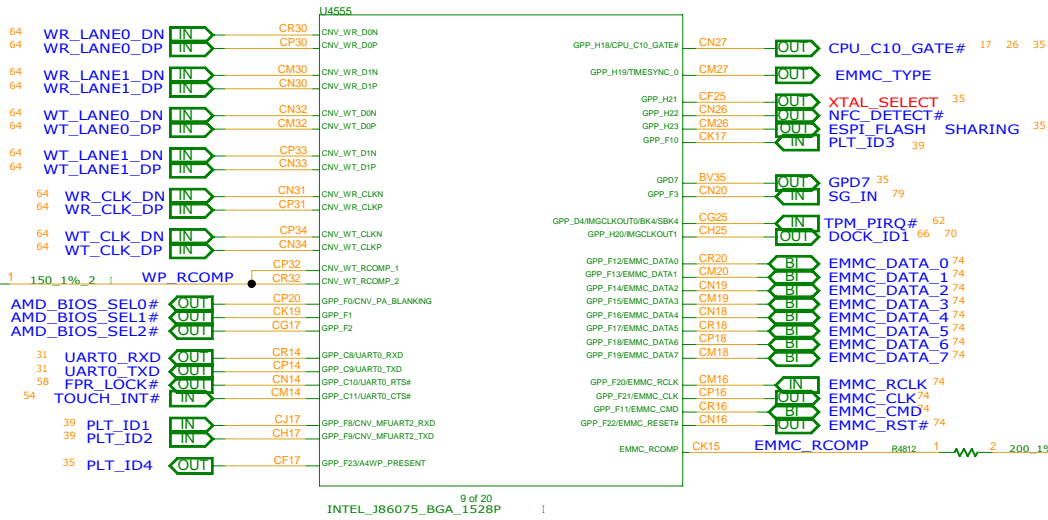
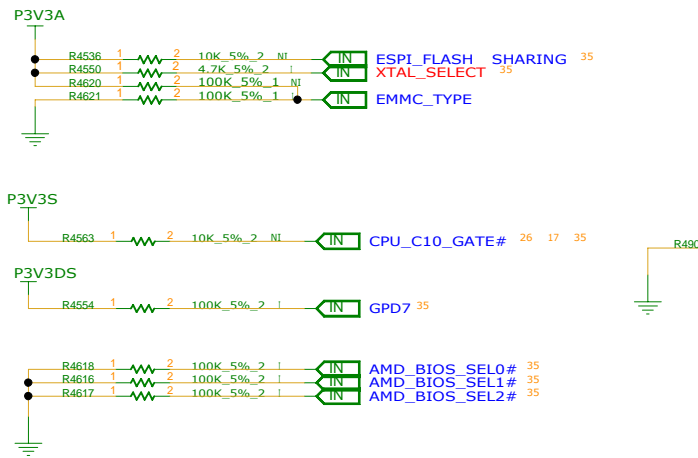
PCB P/N 60xxxxxxxxxx

PCB VER XXX









PLT_ID1	PLT_ID2	PLT_ID3	PLT_ID4	ALS_DETECT#	
0	0	0			CATALONIA VPRO
1	0	0			CATALONIA NONVPRO
	0				CATALONIA
	1				CRESCENT
				1	NON-ALS
				0	ALS
			0		NON-EMMC
			1		EMMC

NOTE : DEFAULT MB IS VPRO , NON-ALS SKU

LOCATION NUMBER:4500~4999

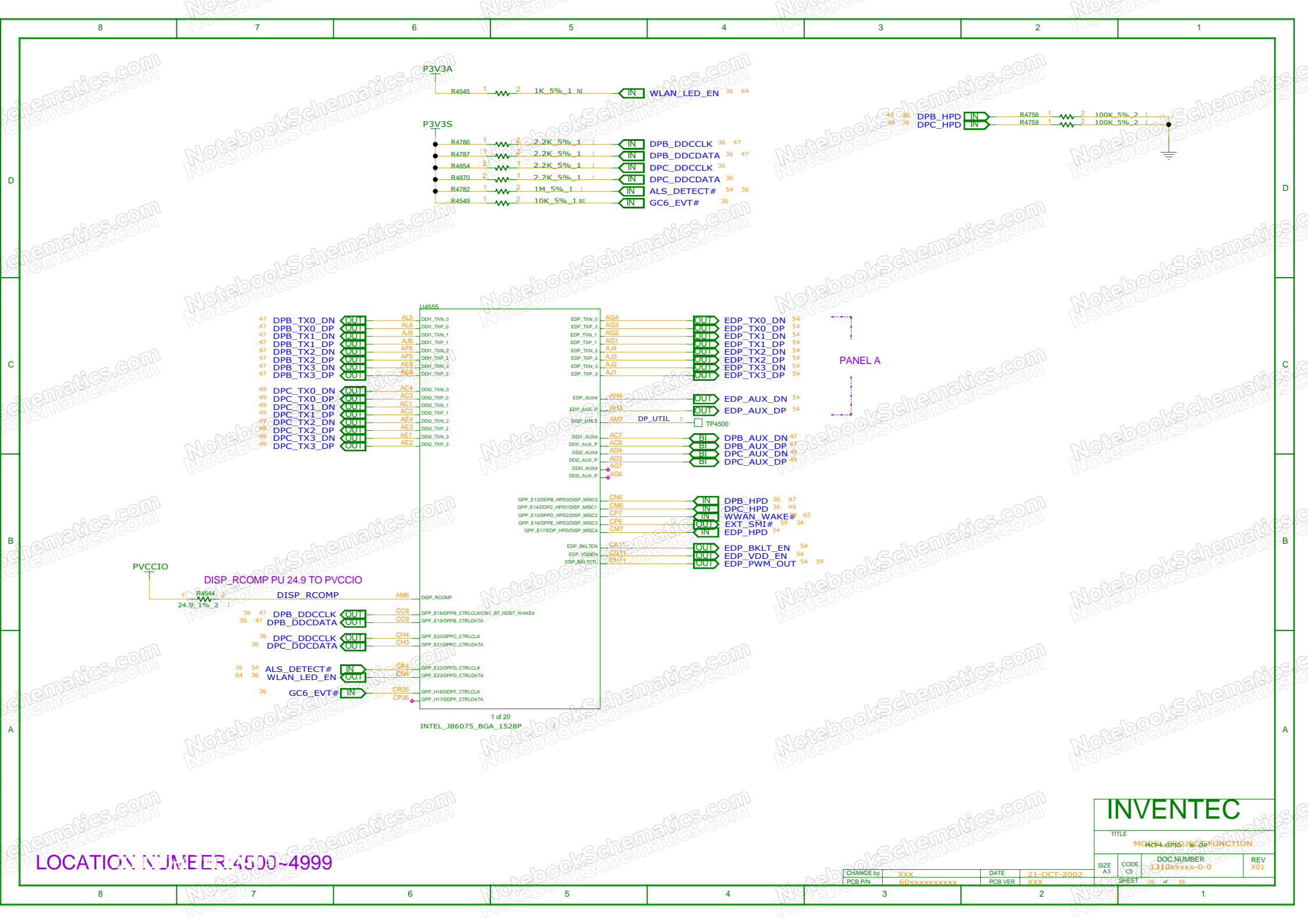
INVENTEC

TITLE  
MODEL PROJECT FUNCTION  
MCP4-GPIO & CNV1

SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
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CHANGE by PCB P/N	XXX 60xxxxxxxxxx	DATE PCB VER	21-OCT-2002 XXX
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LOCATION NUMBER: 4500~4999

INVENTEC				
TITLE				
MC974-6P616-00 FUNCTION				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310xxxx-0-0	X01	
SHEET	36	of 86		

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX





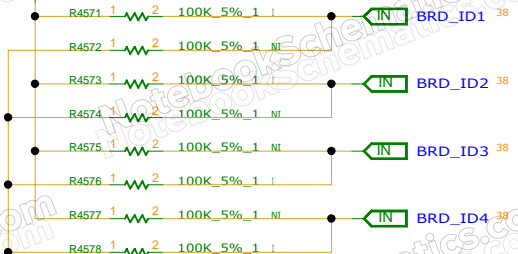
TR  
LAN  
WLAN

DGPU

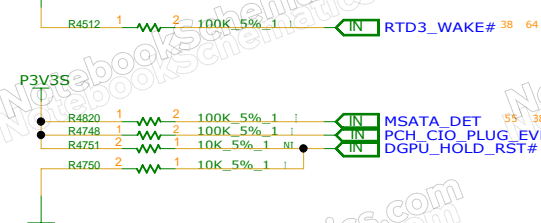
SSD

	BRD_ID1	BRD_ID2	BRD_ID3	BRD_ID4
DB0	0	0	0	0
DB1	0	0	0	1
DB2	0	0	1	0
	0	0	1	1
SI1	0	1	0	0
SI1B	0	1	0	1
SI2	0	1	1	0
	0	1	1	1
SI3	1	0	0	0
PV	1	0	0	1
PVR	1	0	1	0
	1	0	1	1
MV	1	1	0	0

P3V3S



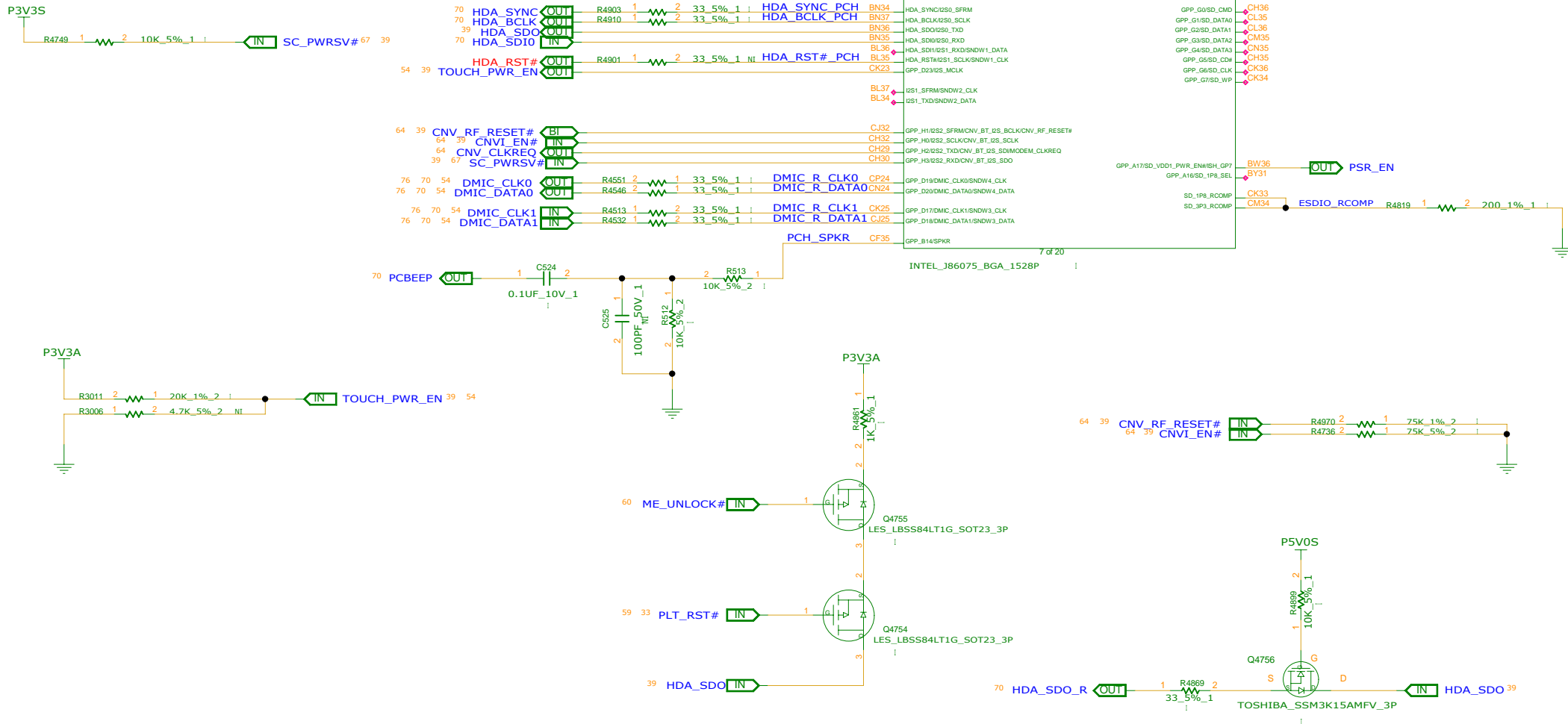
P3V3A



LOCATION NUMBER:4500~4999

INVENTEC			
TITLE			
MODEL, PART & FUNCTION			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
SHEET 38 of 86			

CHANGE by XXX PCB P/N 60xxxxxxxxx DATE 21-OCT-2002 PCB VER XXX



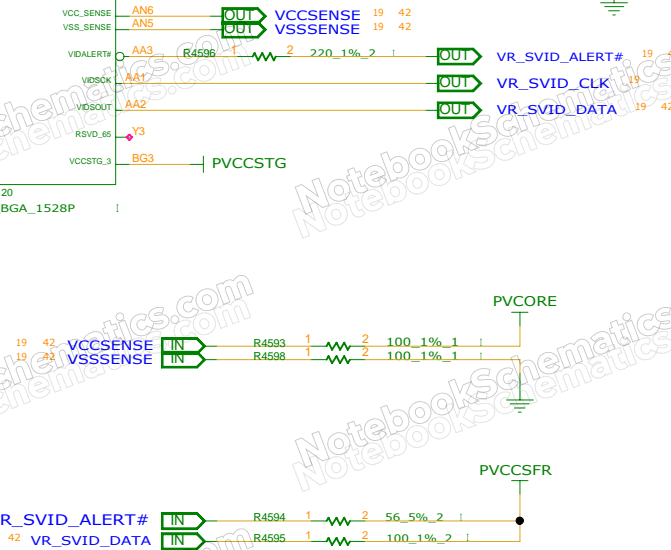
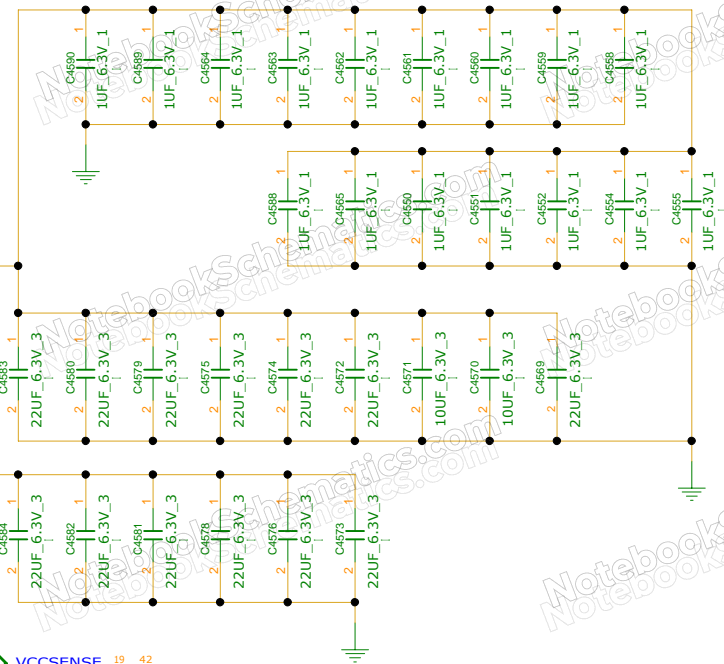
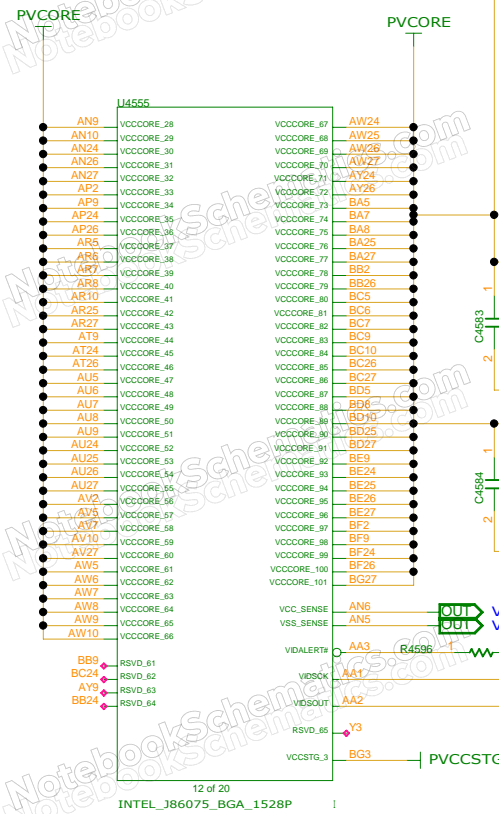
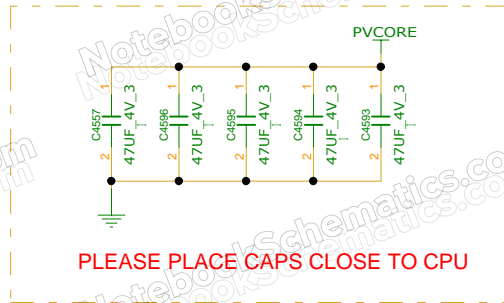
LOCATION NUMBER:4500~4999

INVENTEC				
TITLE				
MODEL PROJECT FUNCTION				
MCP7-RTC & Audio & SATA				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310xxxx-0-0	X01	
CHANGE by		DATE	21-OCT-2002	
PCB P/N		PCB VER	XXX	
60xxxxxxxxxx				
SHEET		39 of 86		









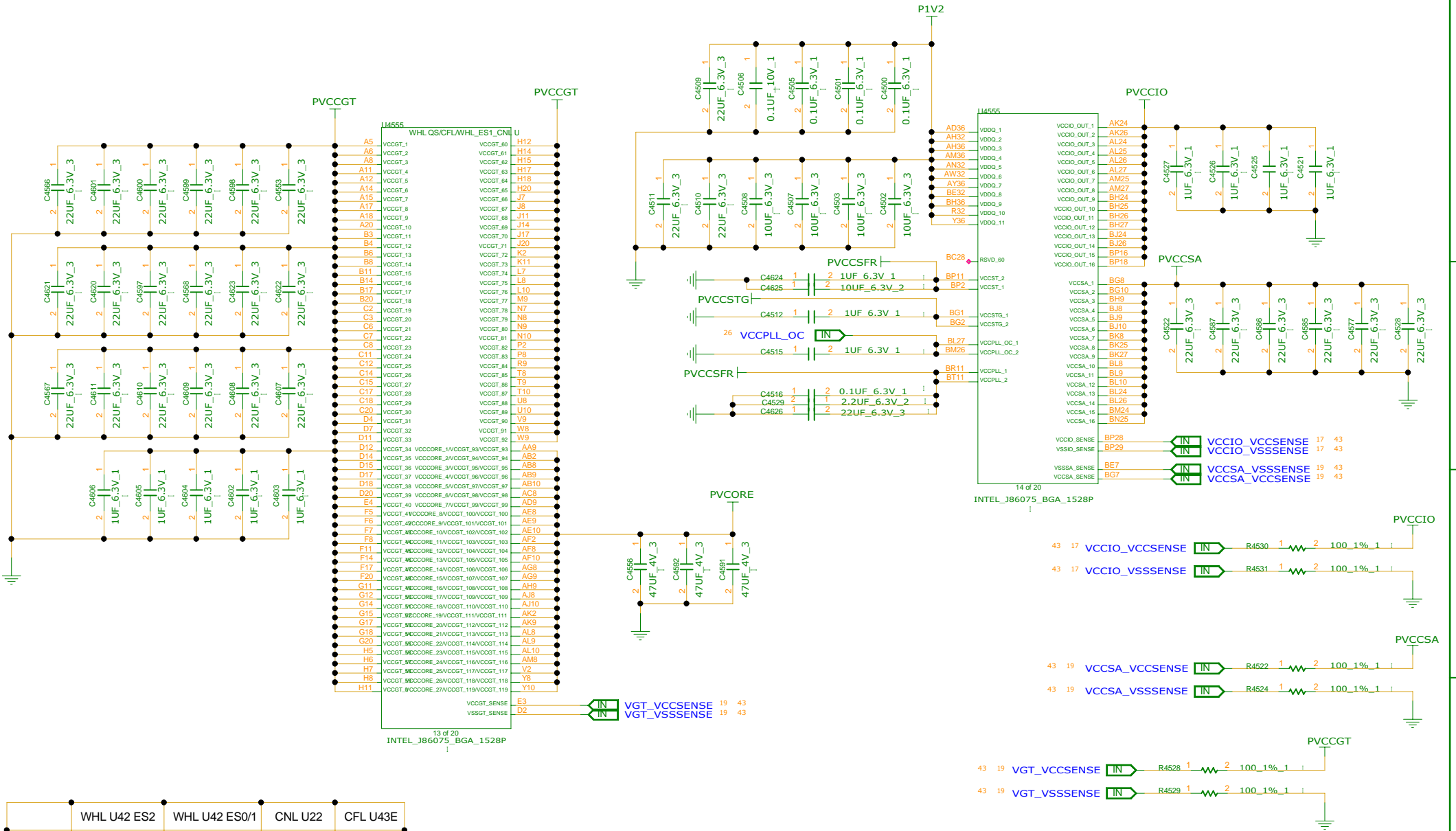
LOCATION NUMBER: 4300~4999

INVENTEC				
TITLE				
MCP60 POWER CT. FUNCTION				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310xxxx-0-0	X01	
SHEET		42	of 86	

CHANGE BY	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxx	PCB VER	XXX

	WHL U42 ES2	WHL U42 ES0/1	CNL U22	CFL U43E
PAD4500	I	NI	NI	NI
PAD4501	NI	I	I	I
R4599	NI	NI	NI	I
R4601	NI	NI	NI	I
R4602	NI	NI	NI	I

LOCATION NUMBER:4500~4999

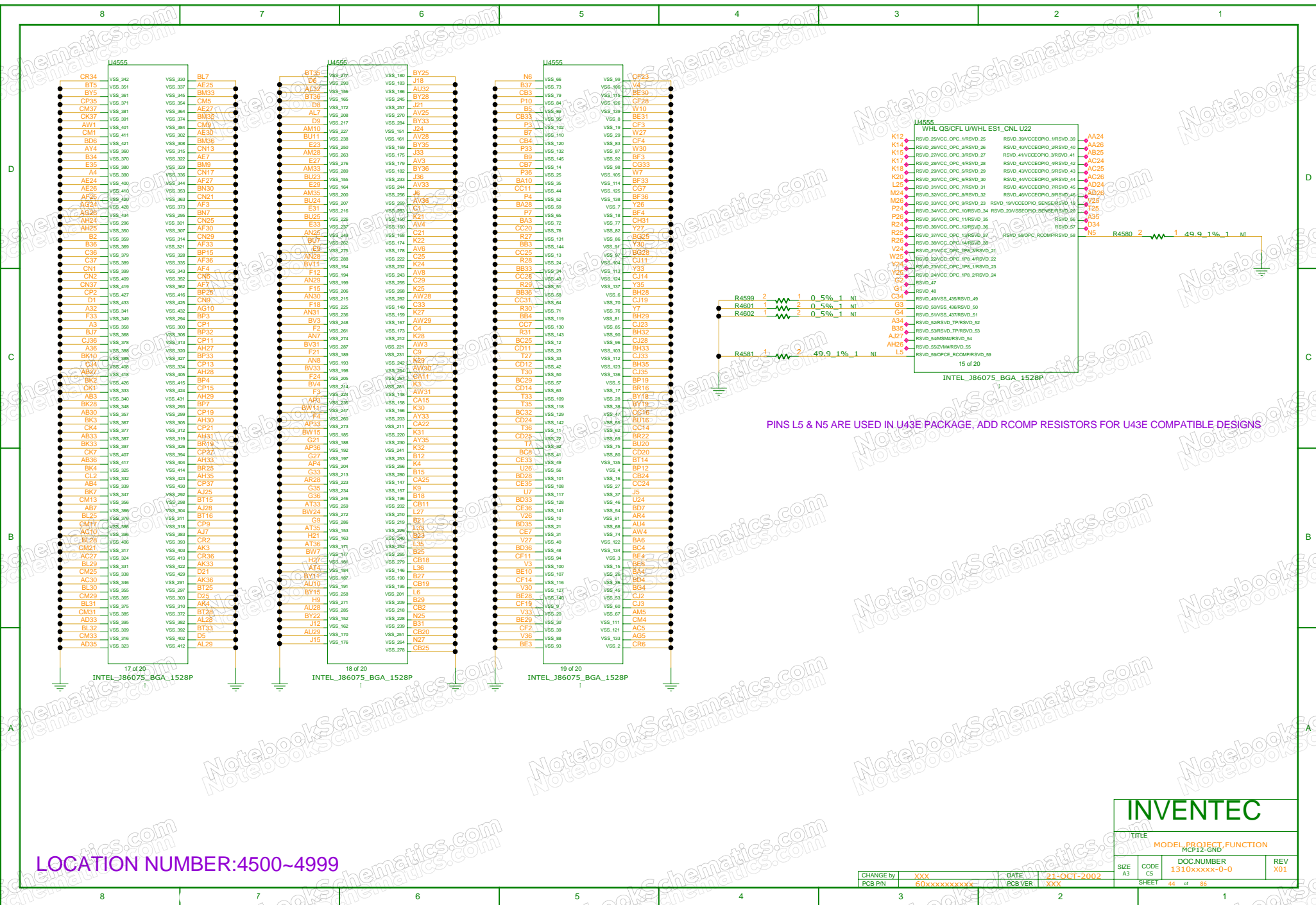


INVENTEC

TITLE  
MCNPL1 POWER

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01  
SHEET 43 of 86

CHANGE by XXX DATE 21-OCT-2002  
PCB P/N 60xxxxxxxxxxx PCB VER XXX



# INVENTEC

TITLE  
MODEL PROJECT FUNCTION  
MCP12-GND

SIZE A3 CODE CS  
SHEET 44 of 86

REV X01

CHANGE by XXXX  
PCB PIN 60XXXXXXXXXX

DATE 21-OCT-2002  
PCB VER XXX

A
B
C
D
E
F



A

1

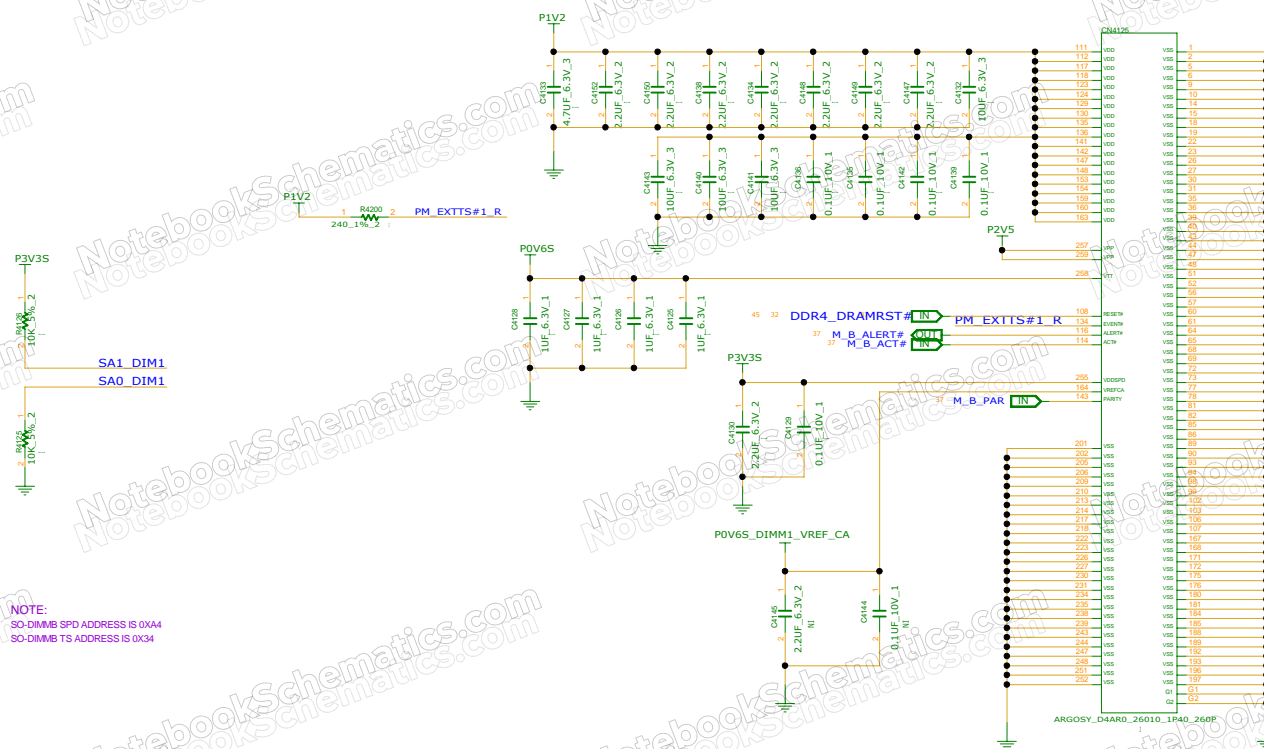


TITLE	MODEL PROJECT FUNCTION
1. <b>Project Overview</b>	1. <b>Project Overview</b>
2. <b>Project Objectives</b>	2. <b>Project Objectives</b>
3. <b>Project Scope</b>	3. <b>Project Scope</b>
4. <b>Project Organization</b>	4. <b>Project Organization</b>
5. <b>Project Schedule</b>	5. <b>Project Schedule</b>
6. <b>Project Budget</b>	6. <b>Project Budget</b>
7. <b>Project Risks</b>	7. <b>Project Risks</b>
8. <b>Project Communication</b>	8. <b>Project Communication</b>
9. <b>Project Monitoring and Control</b>	9. <b>Project Monitoring and Control</b>
10. <b>Project Closure</b>	10. <b>Project Closure</b>

SHEET 45 of 86

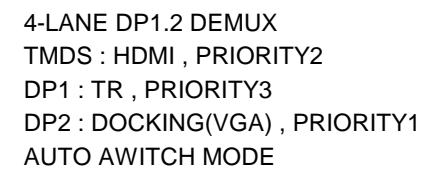
SHEET 45 of 86

## LOCATION NUMBER:4100~4299



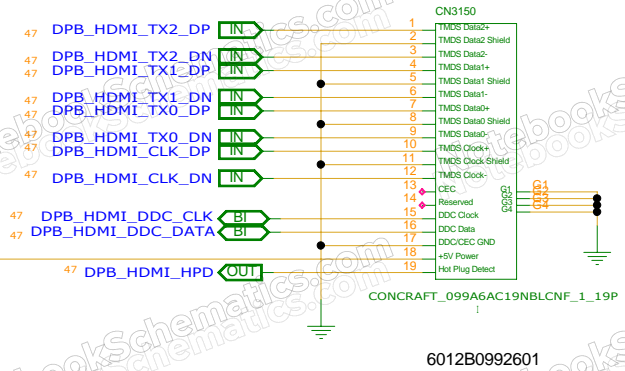
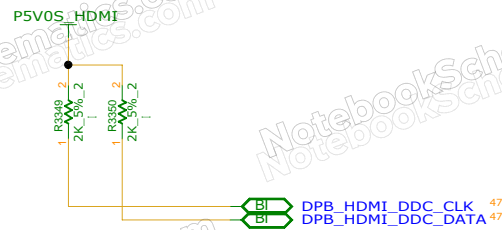
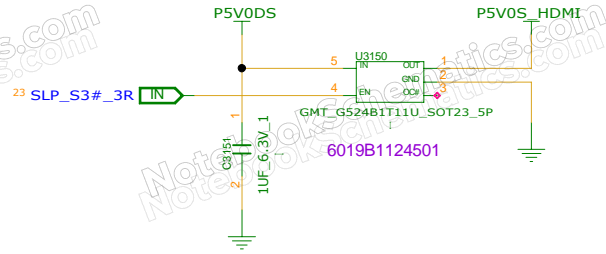
**NOTE:**  
SO-DIMM SPD ADDRESS IS 0XA  
SO-DIMM TS ADDRESS IS 0X34





CHANGE b	xxx	DATE	21-OCT-2002
PCB PIN	60xxxxxxxxxx	PCB VER	xxx

# HDMI



LOCATION NUMBER:3150~3299

INVENTEC

TITLE MODEL,PROJECT,FUNCTION

HDMI

SIZE A3

CODE CS

DOC NUMBER 1310xxxx-0-0

REV X01

CHANGE by XXX

PCB P/N 60xxxxxxx

DATE 21-OCT-2002

PCB VER XXX

SHEET 48

of 86

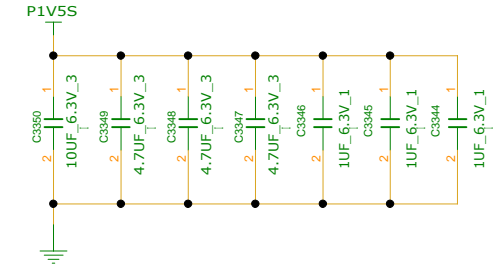
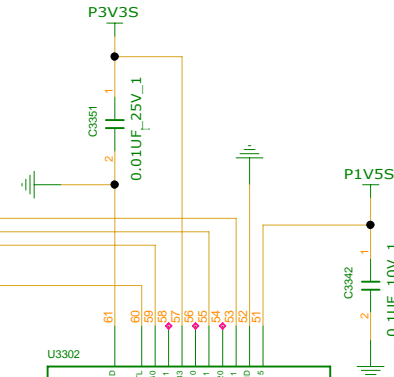
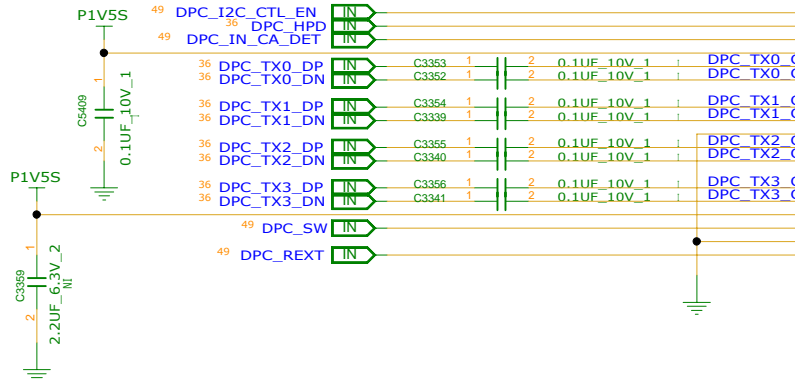
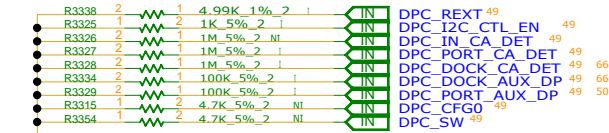
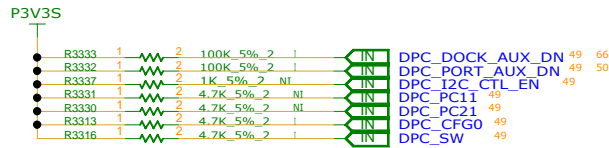


# 4-LANE DP1.2 DEMUX

DP1 : TR , PRIORITY2  
DP2 : DOCKING(DP) , PRIORITY1

I2C\_CTL\_EN TABLE

	SMBUS	PIN CONTROL
R3337	I	NI
R3325	NI	I
R3336	I	NI
R3335	I	NI
ADDRESS:0X58/59		



PARADE\_PS8338AQFN60GTR\_QFN\_60P

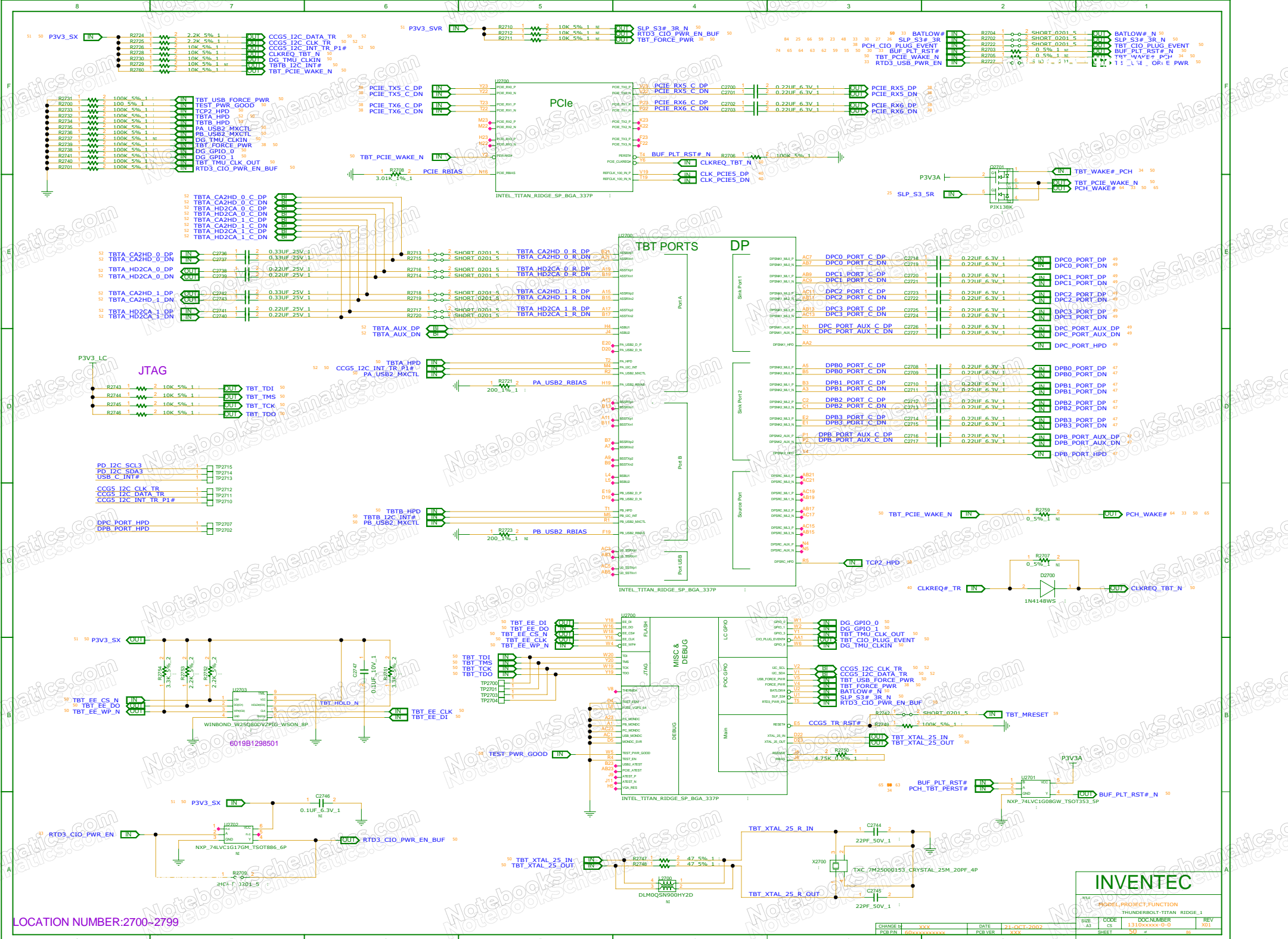
6019B1101001

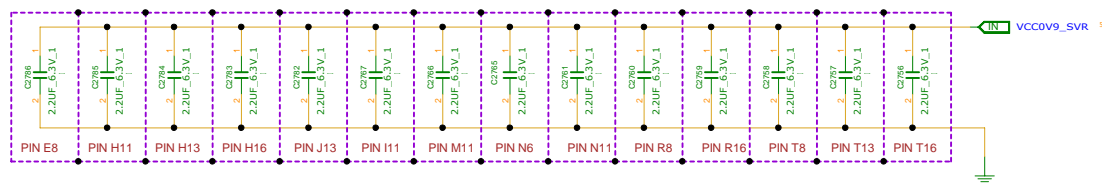
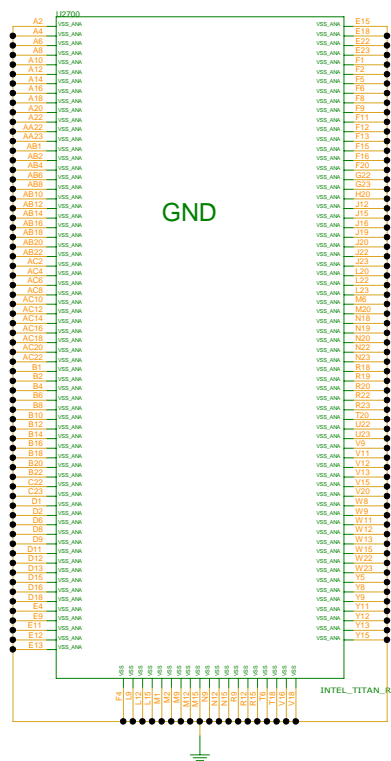
INVENTEC

LOCATION NUMBER:3300~3399

CHANGE by	XXX	DATE	
PCB P/N	60N6xxxxxxx	PCB VER	XXX-OCT-2002

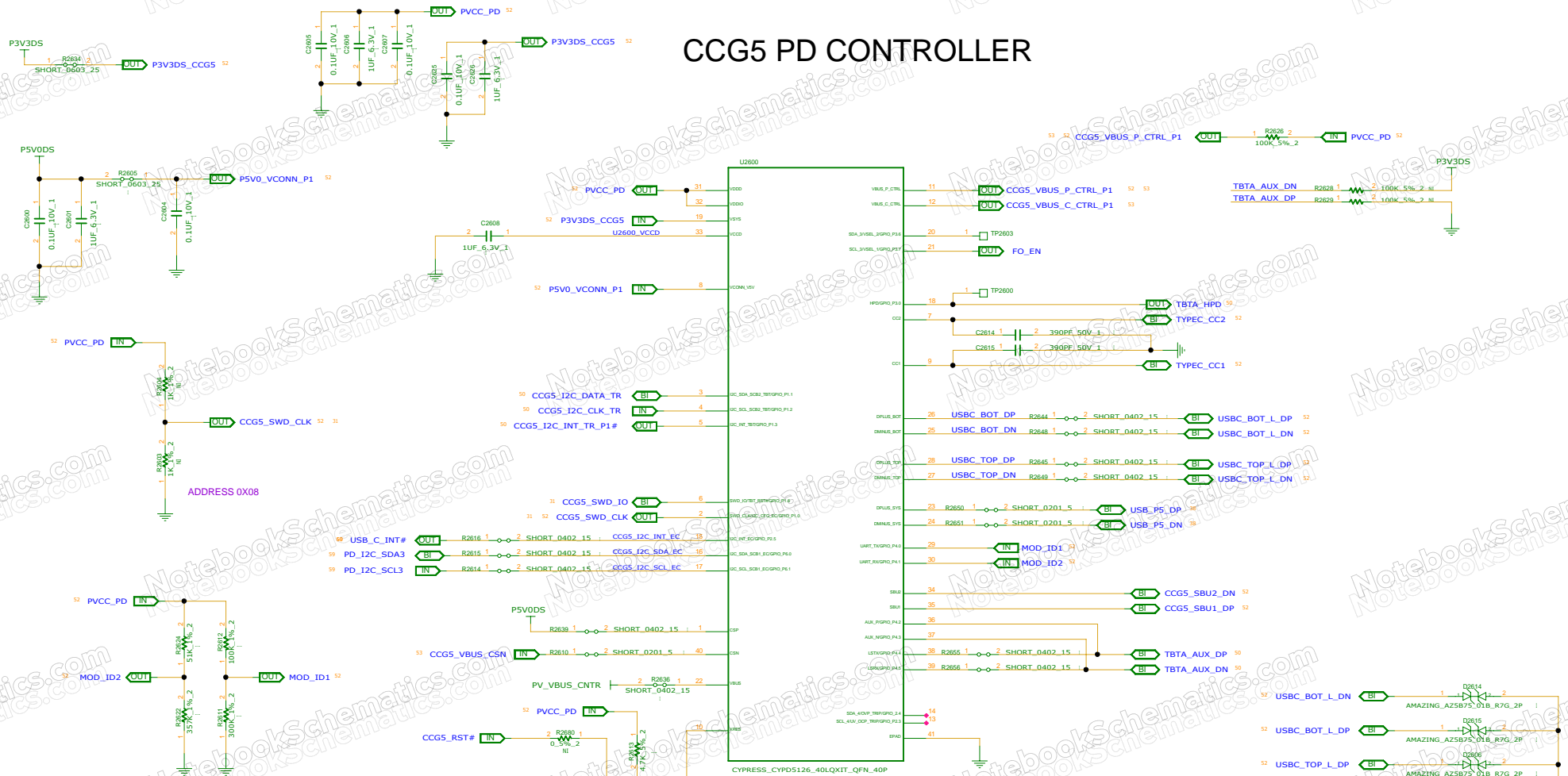
TITLE			
MODEL PROJECT FUNCTION			
DPC DEMUX2 TO VGA			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		of 49	86





INVENTEC				
TITLE				
MODEL,PROJECT,FUNCTION				
THUNDERBOLT-TITAN RIDGE_1				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310XXXX-0-0	X01	
SHEET		51	85	

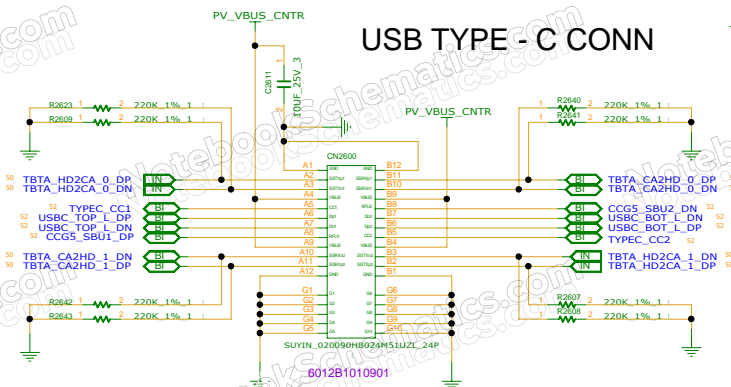
# CCG5 PD CONTROLLER



$$\text{MOD\_ID1} = L6 = 6 \cdot \text{VDD}/8 = 2.475\text{V}$$

$$\text{MOD\_ID2} = L7 = 7 \cdot \text{VDD}/8 = 2.8875\text{V}$$

## USB TYPE - C CONN



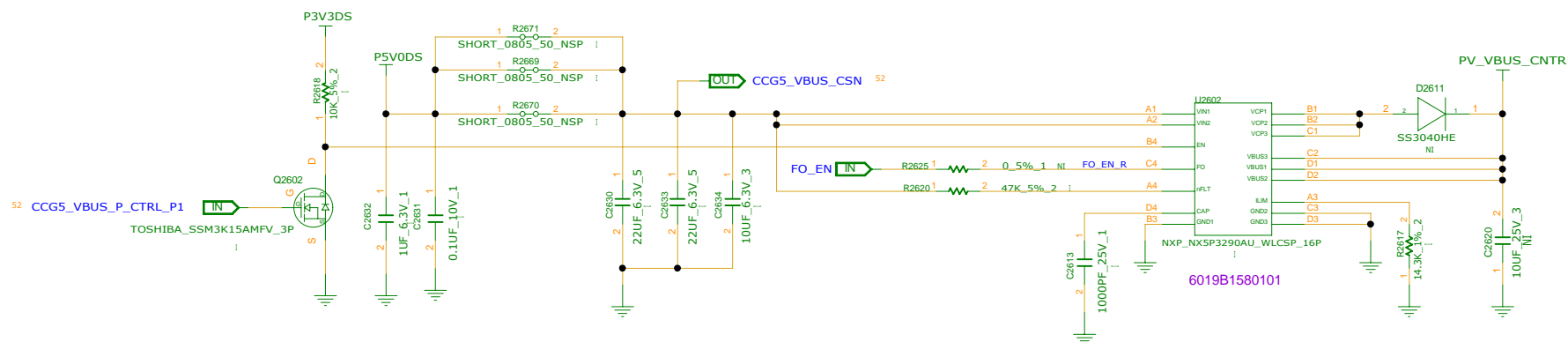
LOCATION NUMBER:2600~2699

INVENTEC

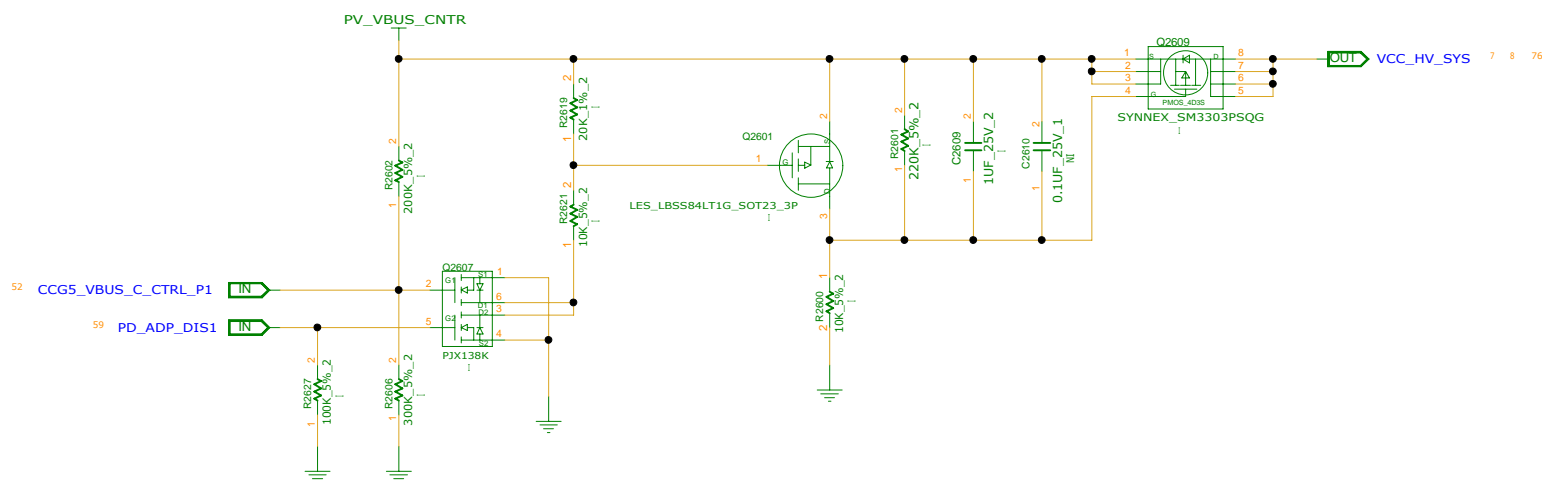
REV	MODEL/PROJECT/FUNCTION	DOC NUMBER	REV
1	USB_TYPEC_1	1310100000-0-0	1
SIZE	CS	SHEET	1

CHANGE NO	DATE	21-OCT-2002
PCB PIN	PCB VER	XXXX

## LOW VOLTAGE PATH



## HIGH VOLTAGE PATH



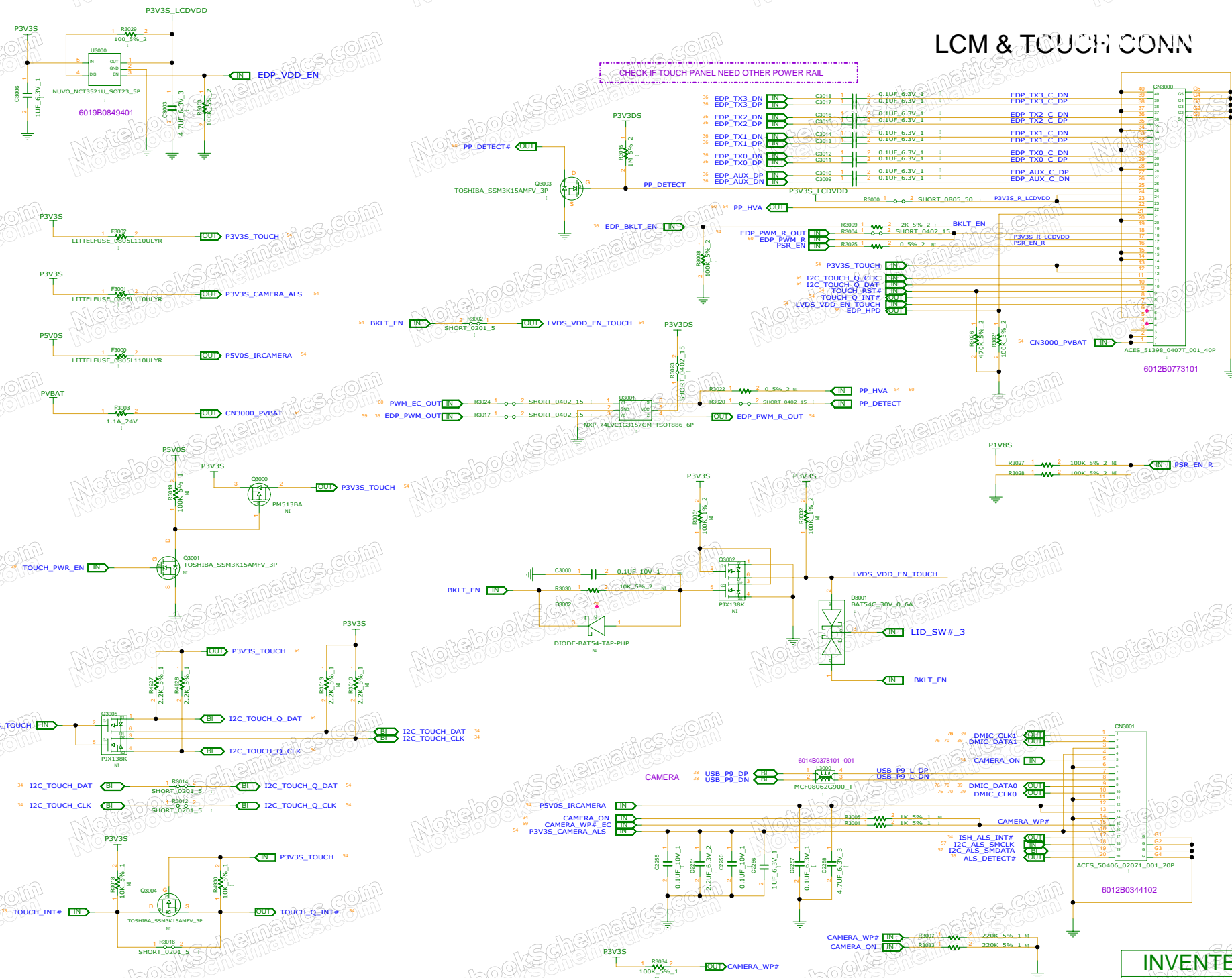
LOCATION NUMBER:2600~2699

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
USB_TYPE C-2 (PORT0)			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET		53	of 86

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60XXXXXXXXXX	PCB VER	XXX

## LCM & TOUCH CONN



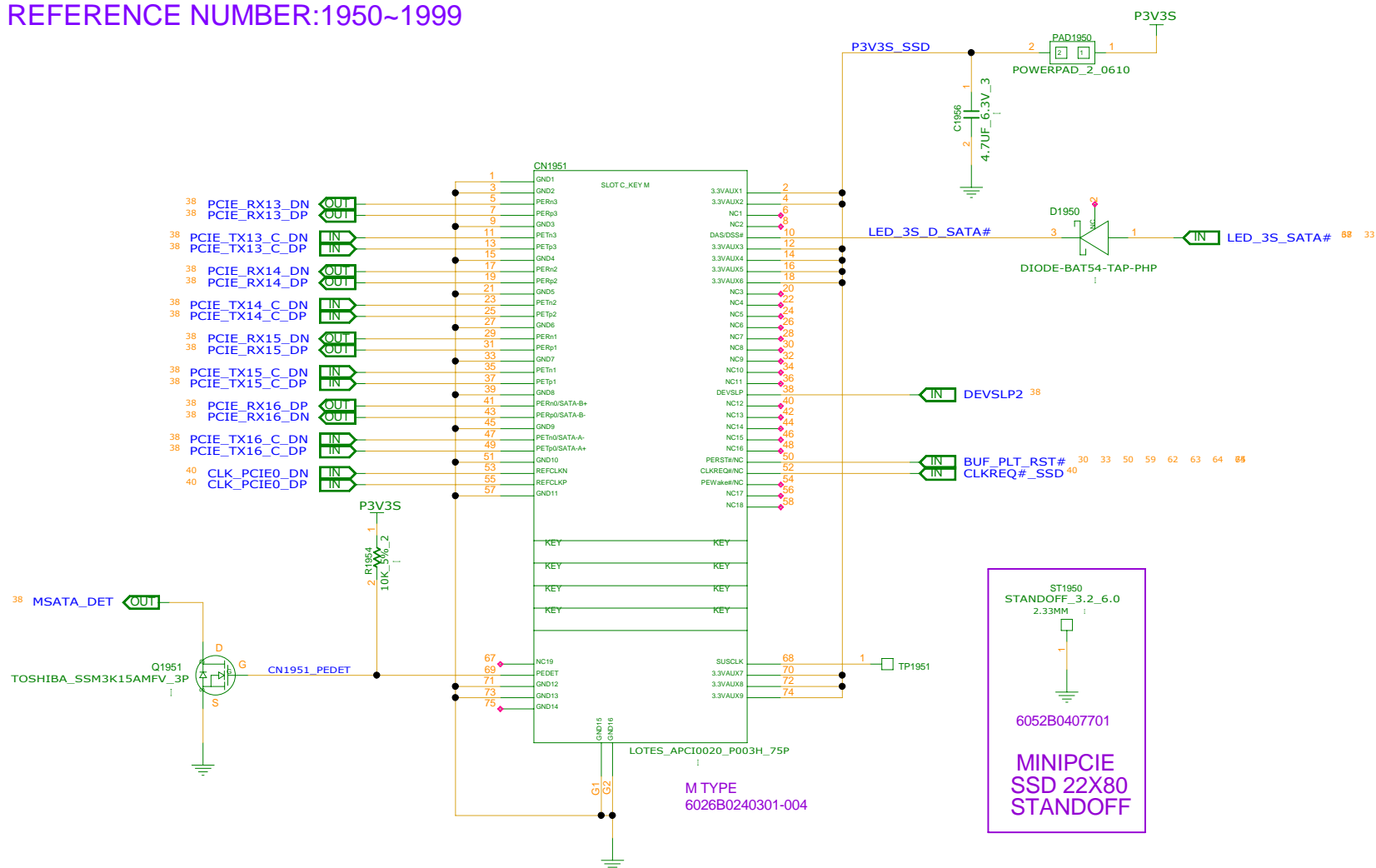
LOCATION NUMBER:3000~3049

INVENTEC			
TITLE MODEL, PROJECT, FUNCTION POWER BLOCK DIAGRAM			
SIZE A3	CODE CS	DOC NUMBER 1310XXXX-0-0	REV X01
SHEET 54		OF 86	

CHANGE by	XXX	DATE	21-OCT-2002	SIZE	A3	CODE	CS	DOC NUMBER	1310xxxxxx-0-0	REV	X01
PCB P/N	60xxxxxxxxxx	PCB VER	XXX	SHEET	54			of	85		



REFERENCE NUMBER:1950~1999



LOCATION NUMBER: 1350~1999

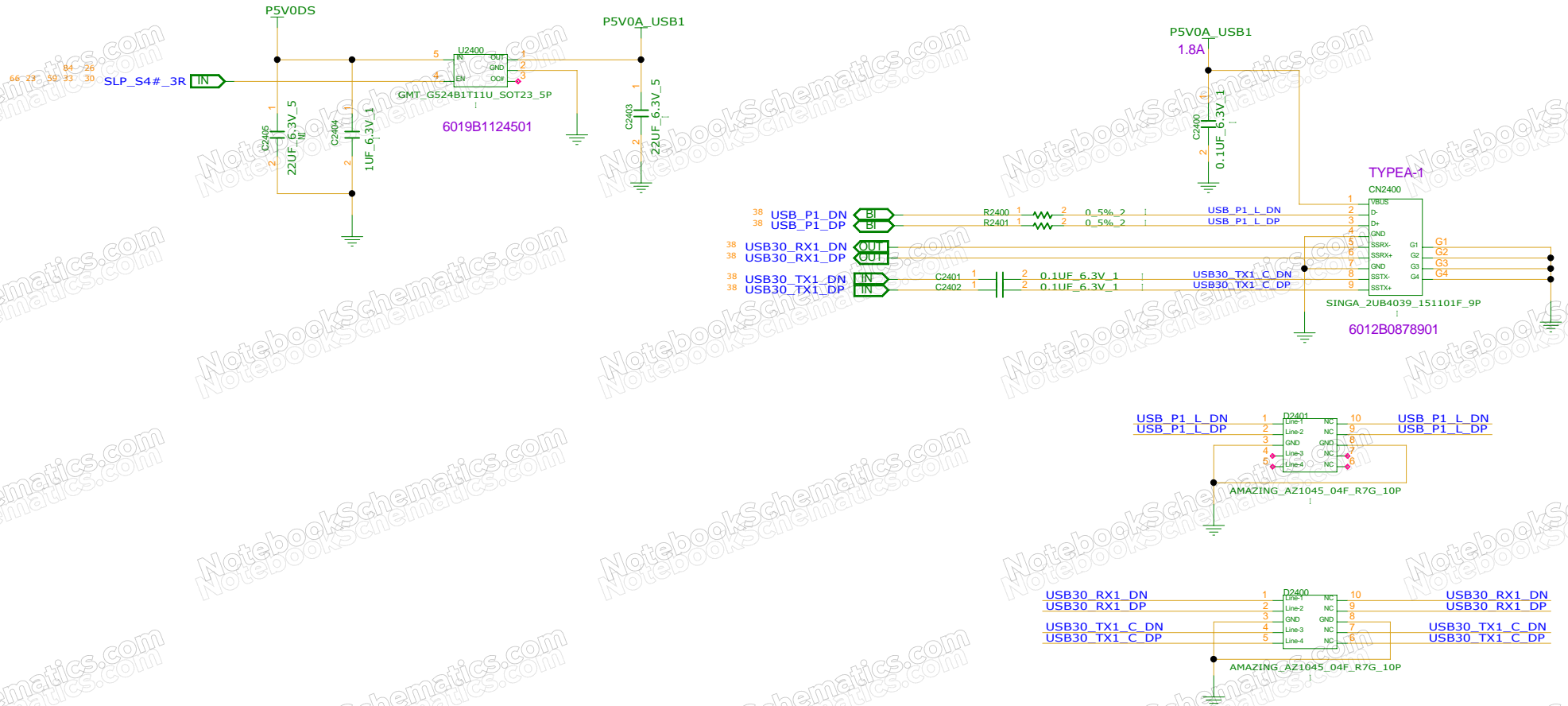
CHANGE by	XXX	DATE	21-OCT-2002	SIZE	A3	CODE CS	1310xxxxx-0-0	X01
PCB P/N	60xxxxxxxxxx	PCB VER	XXX	SHEET	55	of	86	

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION SATA HDD & M-SATA CONN.			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 55 of 86			

# M/B USB3.0 (RIGHT)

REFERENCE NUMBER:2400~2500

LOCATION NUMBER:2400~2499



INVENTEC

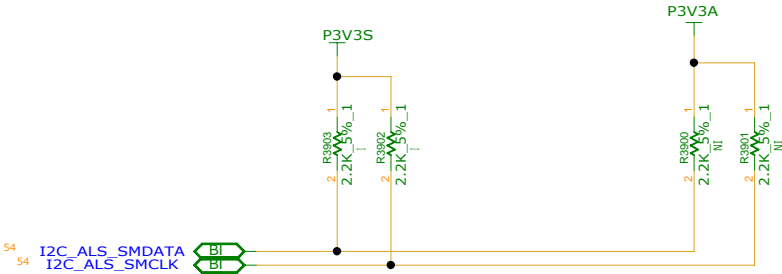
TITLE  
MODEM RIGHT FUNCTION

SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
SHEET	56	of 86	

CHANGE by PCB P/N	XXX 60xxxxxxxxxx	DATE PCB VER	21-OCT-2002 XXX
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# ALS SENSER



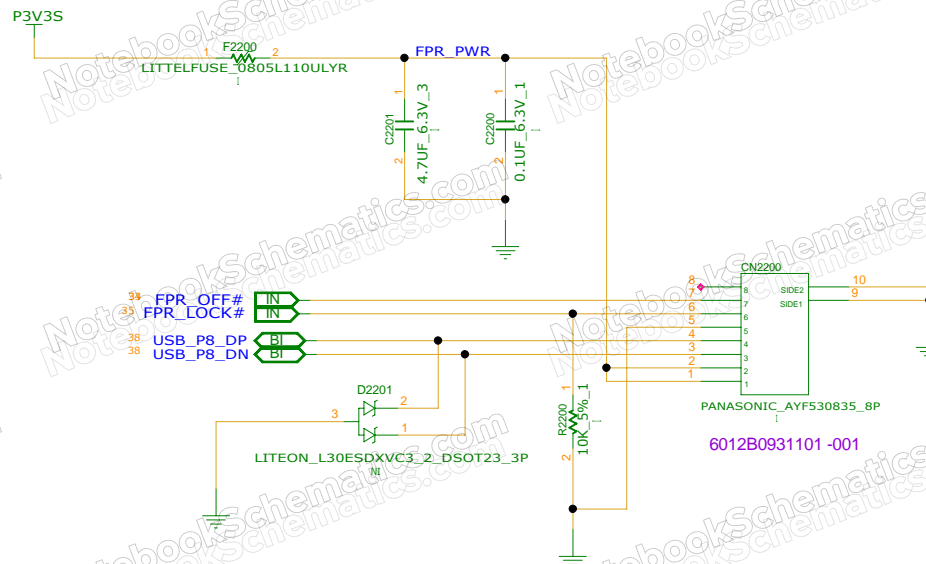
INVENTEC

TITLE  
MODEL,PROJECT,FUNCTION  
ALS

SIZE A3 CODE CS DOC.NUMBER 1310xxxxx-0-0 REV X01

SHEET 57 of 86

CHANGE by XXX DATE 21-OCT-2002  
PCB P/N 60xxxxxxxxxxx PCB VER XXX



## FINGER PRINT CONN

LOCATION NUMBER:2200-2249

**INVENTEC**

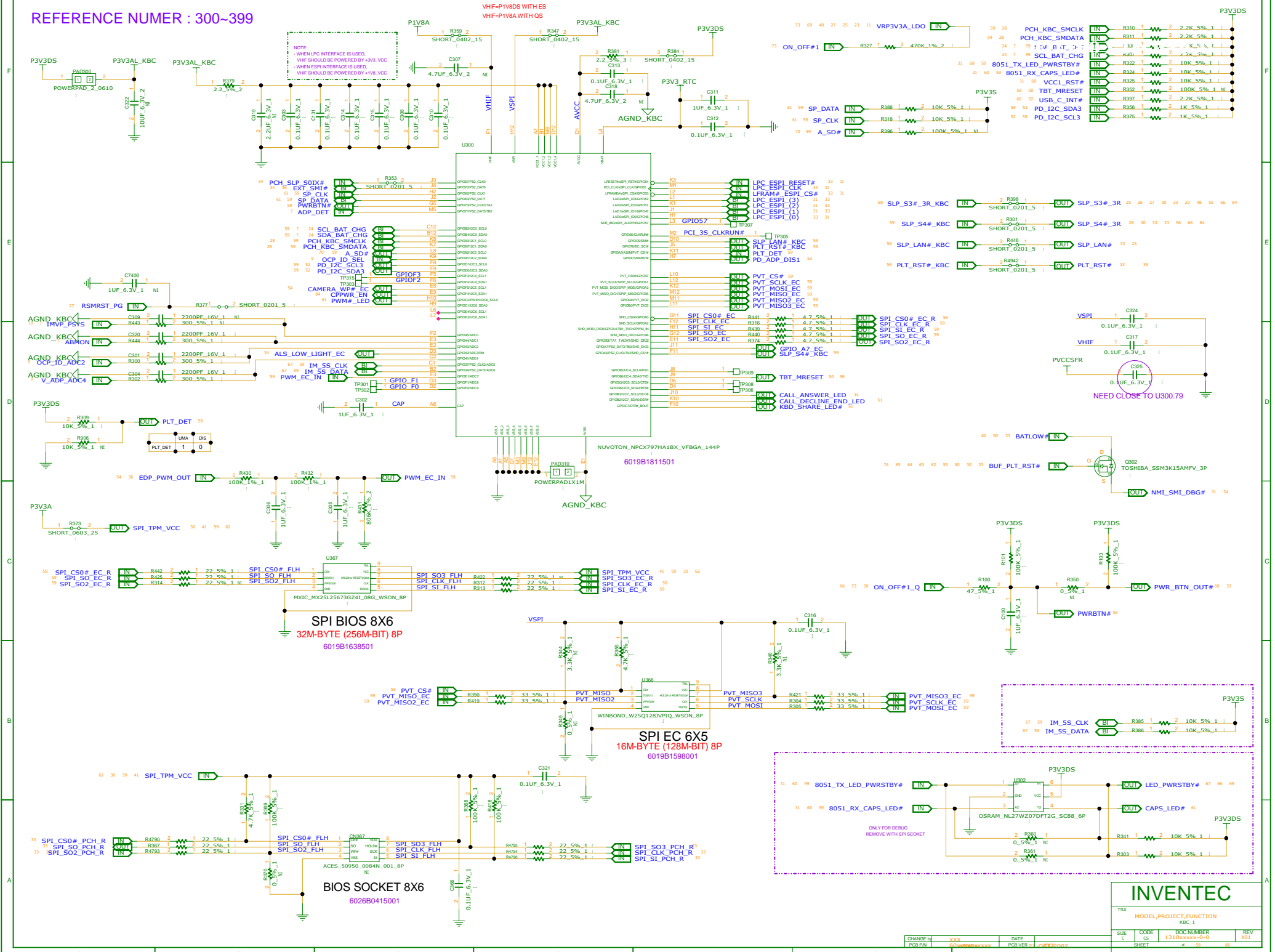
TITLE  
FINGER PRINT CONN

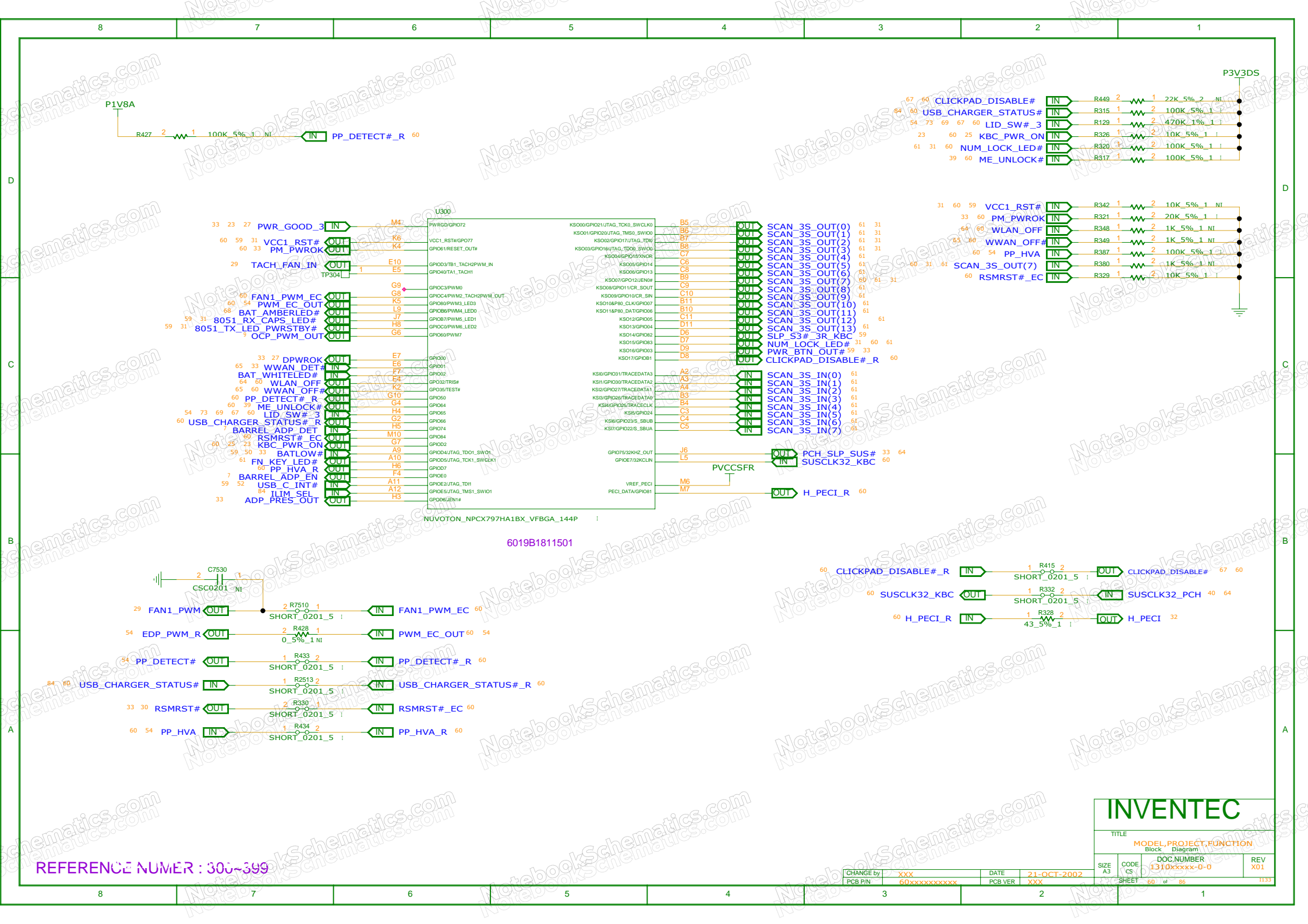
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 58 of 86			

CHANGE by XXX	DATE 21-OCT-2002
PCB P/N 60xxxxxxx	PCB VER XXX

# REFERENCE NUMBER : 300~399

VHIF=P1V8DS WITH ES  
VHIF=P1V8A WITH OS

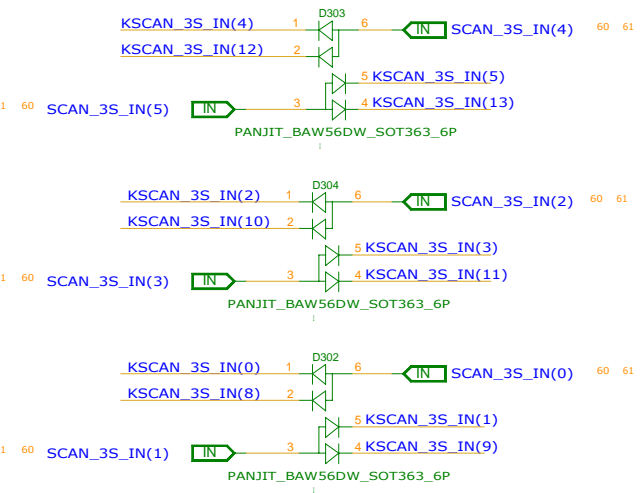
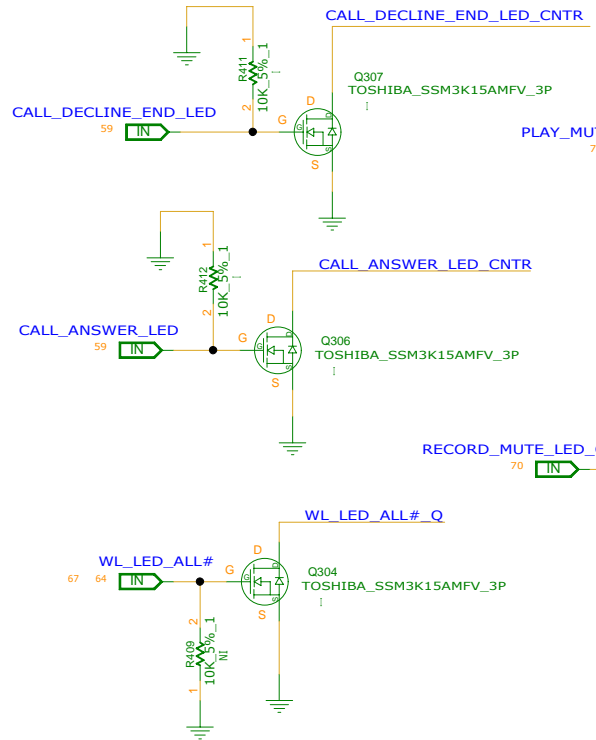




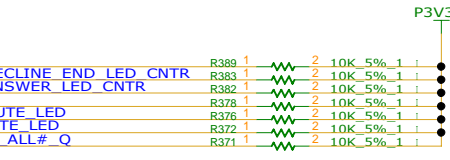
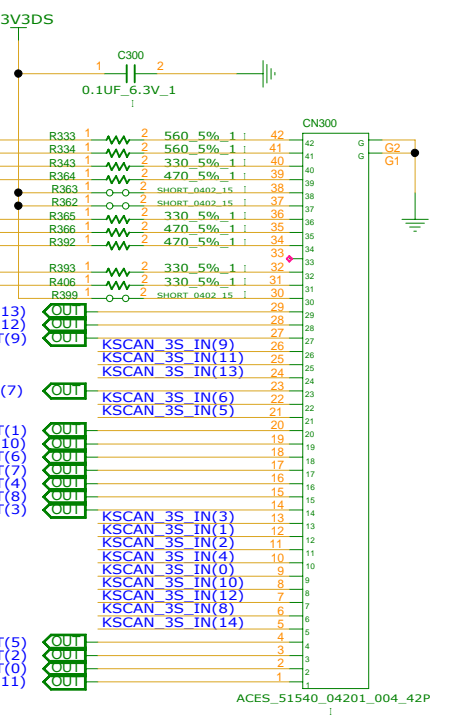
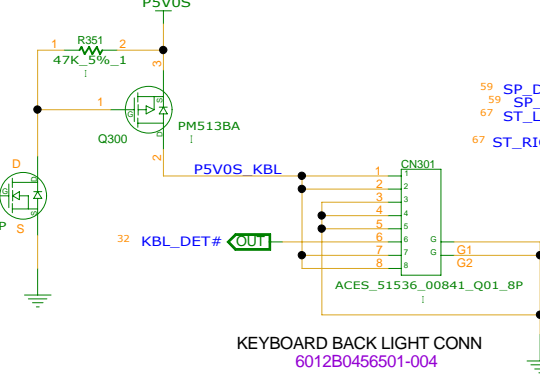
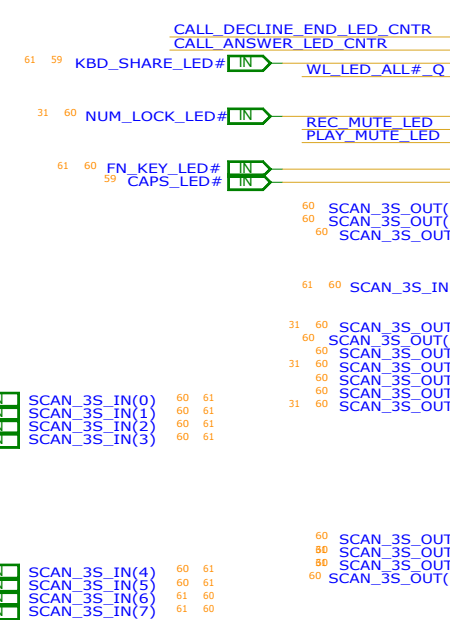
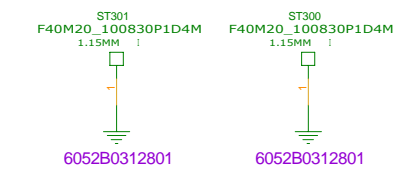
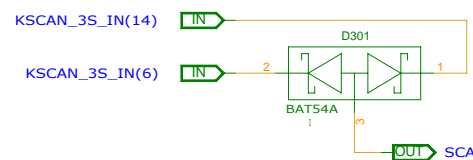
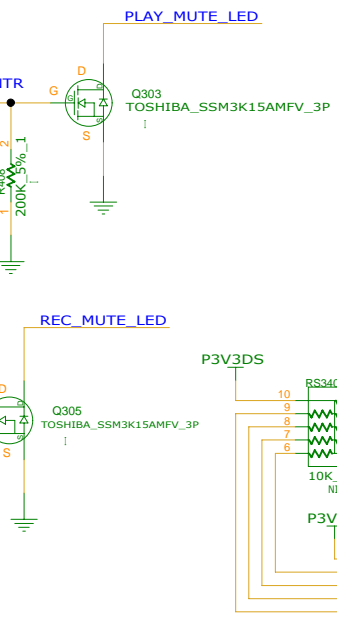
REFERENCE NUMBER : 300~399

INVENTEC				
TITLE				
MODEL,PROJECT,FUNCTION				
Block Diagram				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310xxxx-0-0	X01	
SHEET	60	of 86	1133	

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxx	PCB VER	XXX



LOCATION NUMBER:400~499



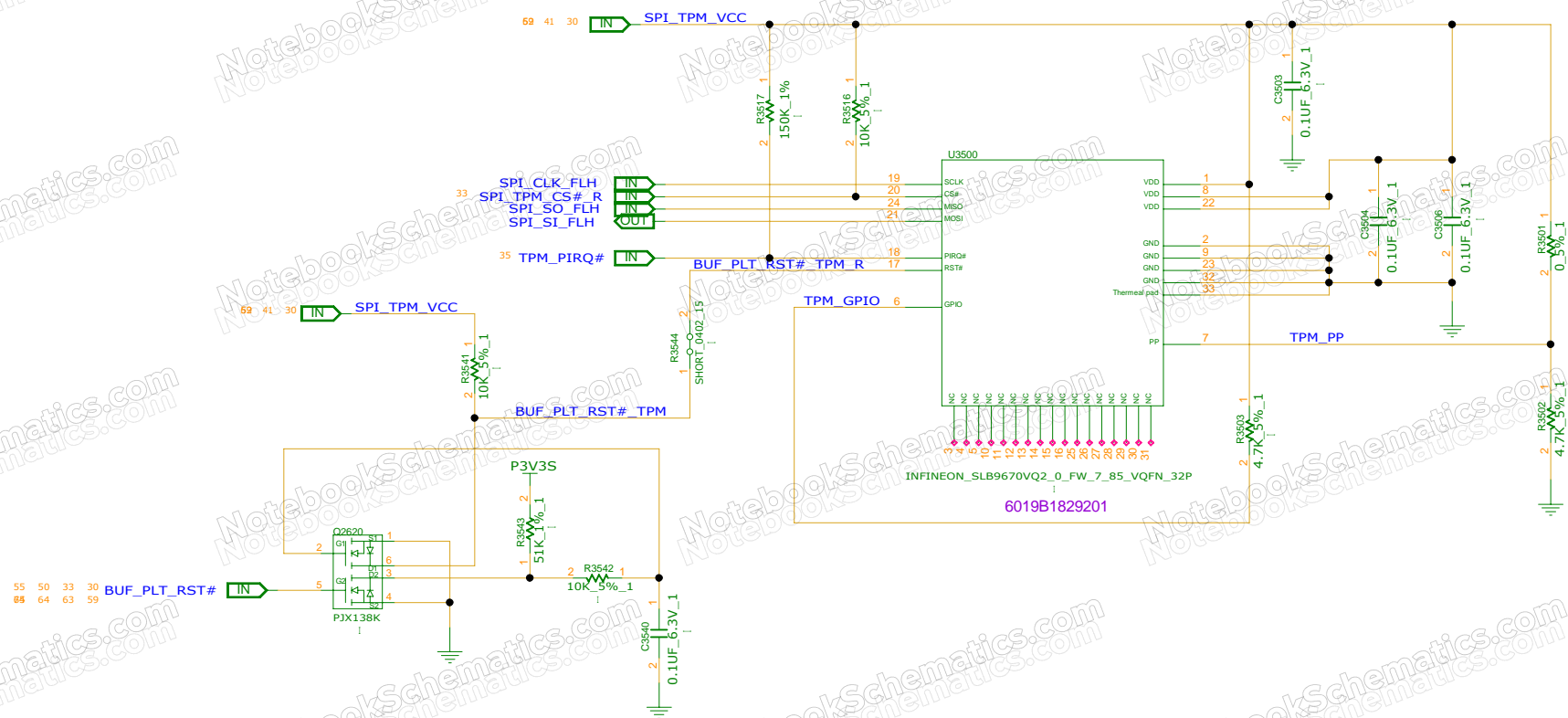
STICK POINT CONN

INVENTEC

TITLE			
MODEL BOARD			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		61 of 86	

CHANGE by	XXX	DATE	21-OCT-2002
PCB PIN	60xxxxxxxxxx	PCB VER	XXX

# TPM2.0



LOCATION NUMBER:3500~3599

<h1>INVENTEC</h1>			
TITLE <b>MODEL, PROJECT, FUNCTION</b>			
SIZE A3	CODE CS	DOC. NUMBER 1310xxxx-0-0	REV X01
SHEET 62 of 86			

A	B	C	D	E	F
---	---	---	---	---	---



A

AA

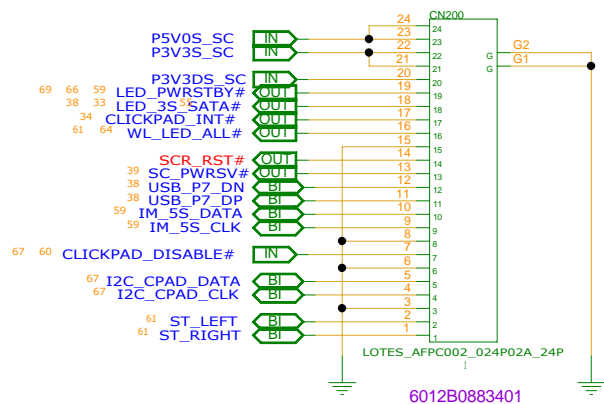






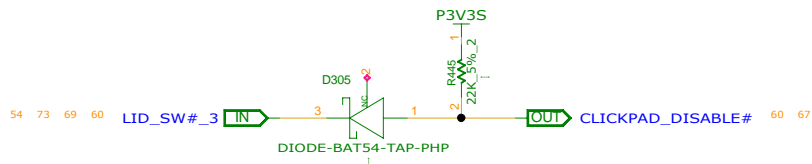
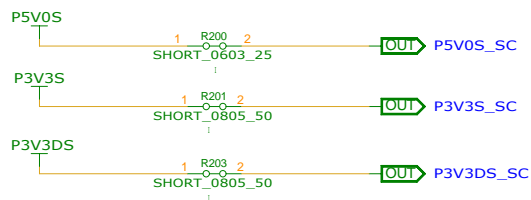




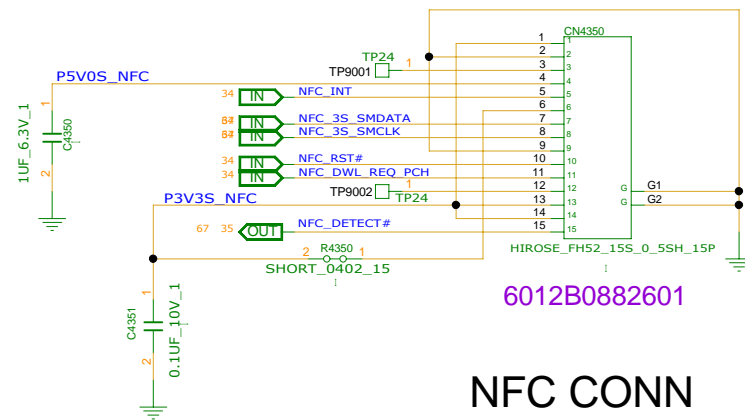


## SMARTCARD CONN

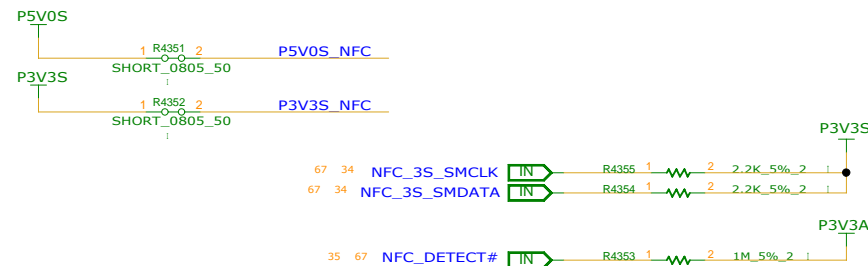
NOTE : CONNECT TO CN9000 (P.79)  
SMART CARD AND TOUCHPAD D/B W TO B CONN



LOCATION NUMBER:850~999



## NFC CONN



LOCATION NUMBER:4350~4399

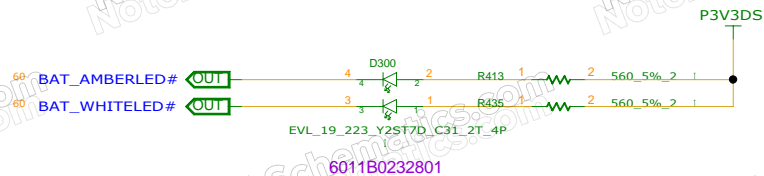
INVENTEC

TITLE  
STORE POINT G26 FUNCTION

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 67 of 86			

CHANGE by PCB P/N	XXX 60xxxxxxxxxxx	DATE PCB VER	21-OCT-2002 XXX
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POWER JACK LED



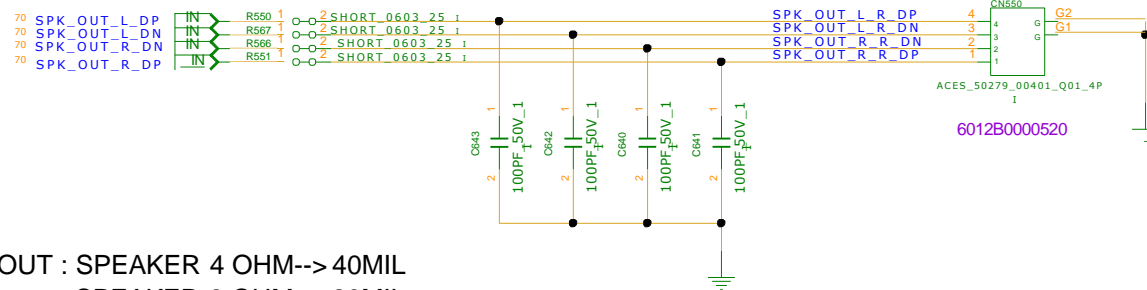
LOCATION NUMBER:100~199

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
POWER JACK LED			
DOC NUMBER			
1310xxxx-0-0			
REV			
X01			
SIZE	CODE	SHEET	
A3	CS	68 of 86	

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX







LAYOUT : SPEAKER 4 OHM--> 40MIL  
SPEAKER 8 OHM--> 20MIL

# INTERNAL SPEAKER

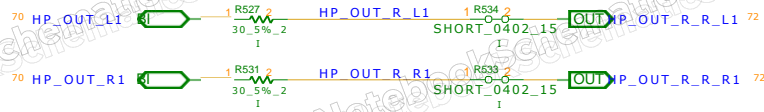
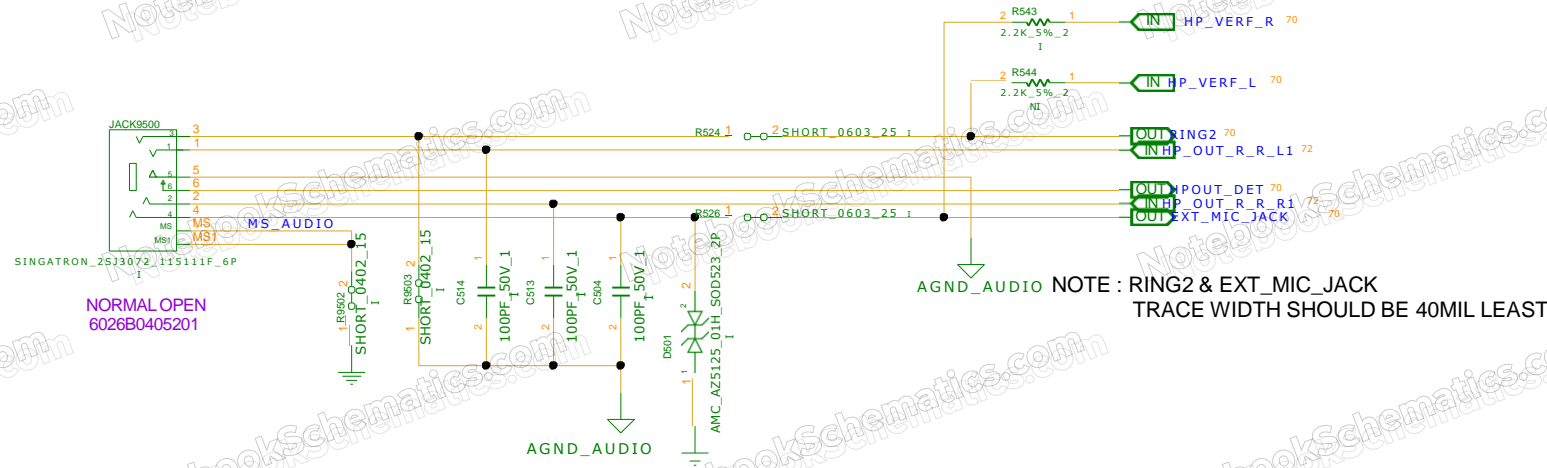
REFERENCE NUMBER:600~649

INVENTEC

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	Eletro-XTech	PCB VER	XXX

SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 71 of 86			

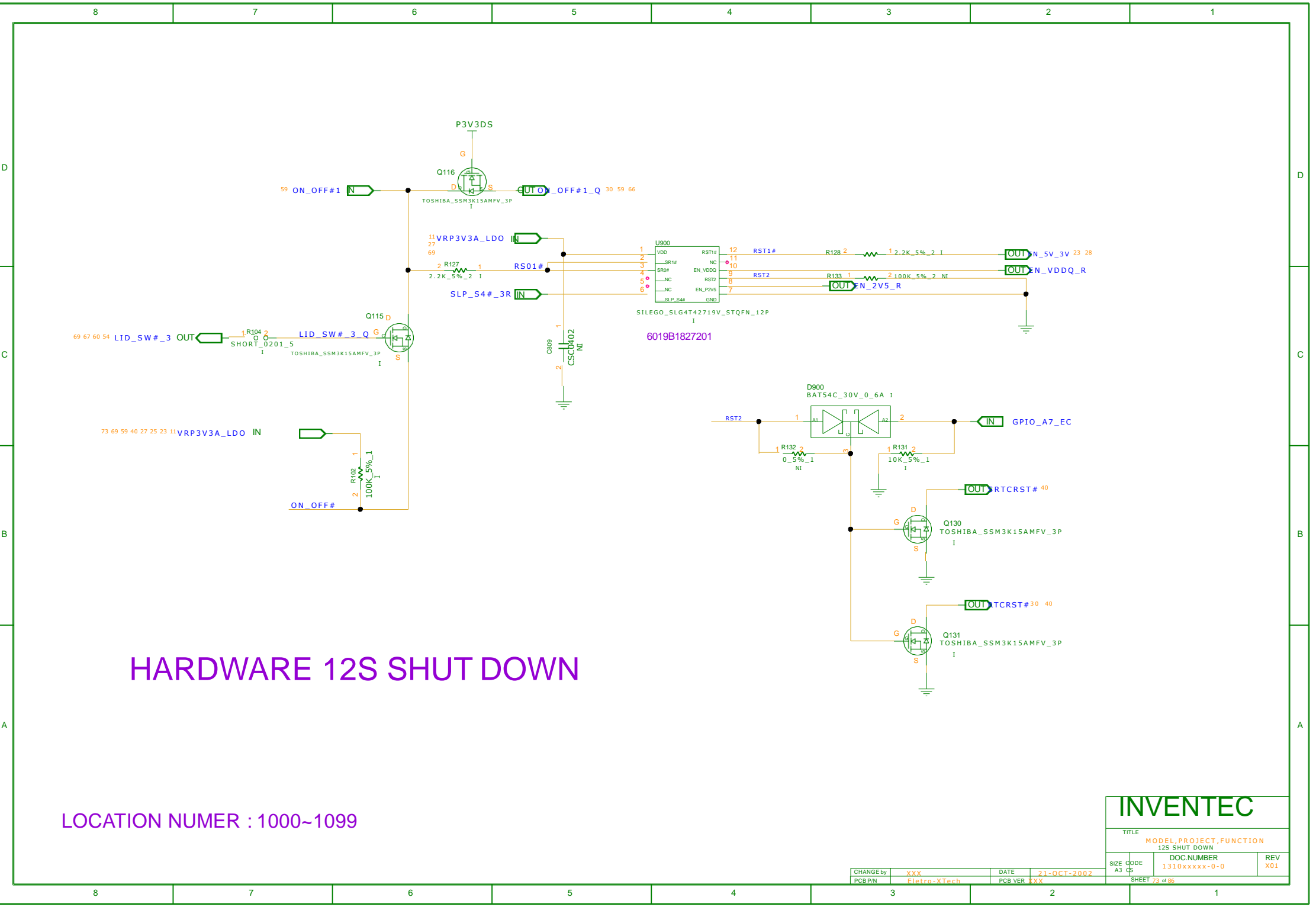




LOCATION NUMER : 500-699

CHANGE BY	XXX	DATE	21-OCT-2002
PCB P/N	Eletro-XTech	PCB VER	XXX

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 72 of 86			

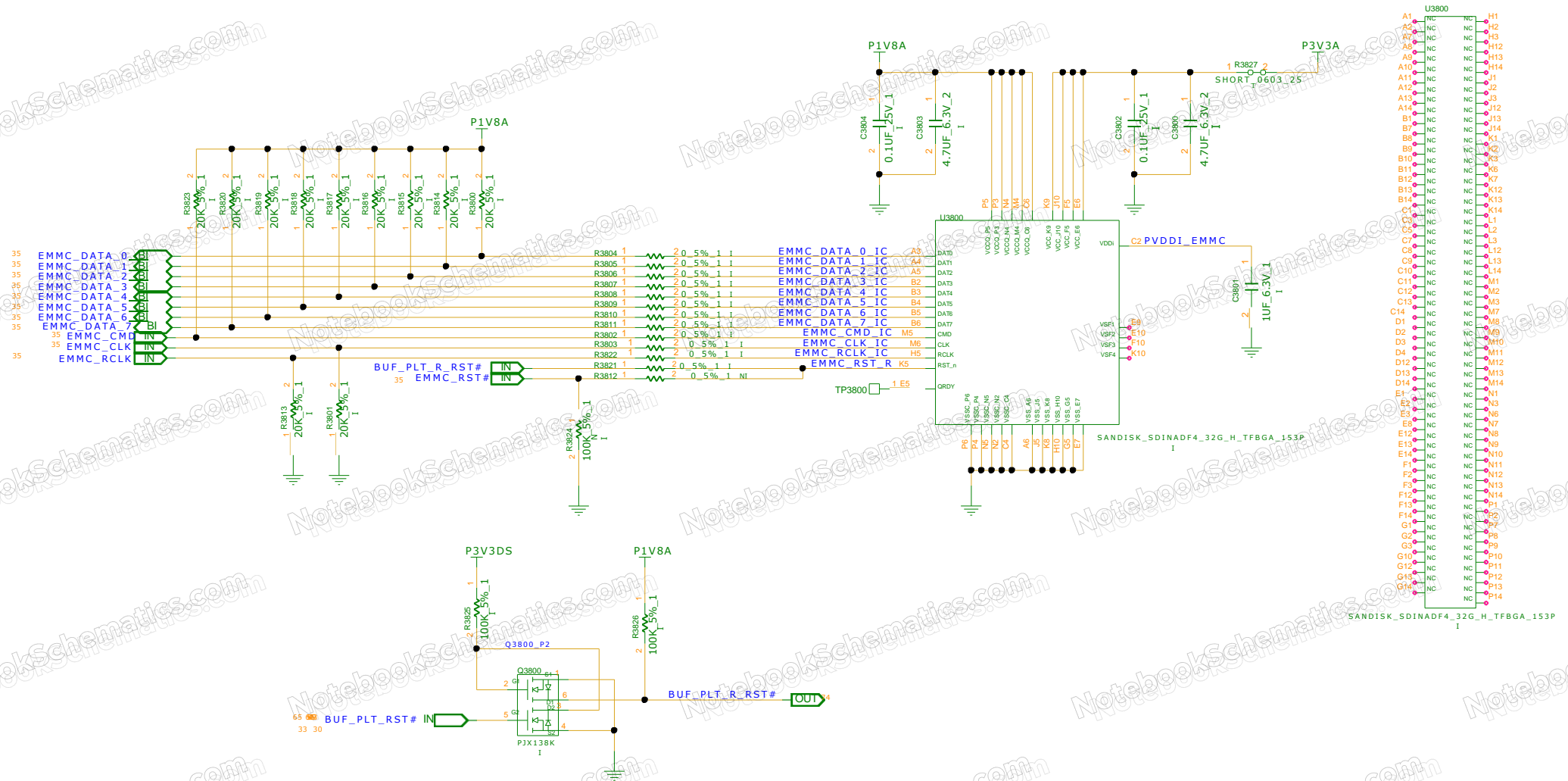


# HARDWARE 12S SHUT DOWN

LOCATION NUMER : 1000~1099

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
12S SHUT DOWN			
SIZE		DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 73 of 86			

# EMMC



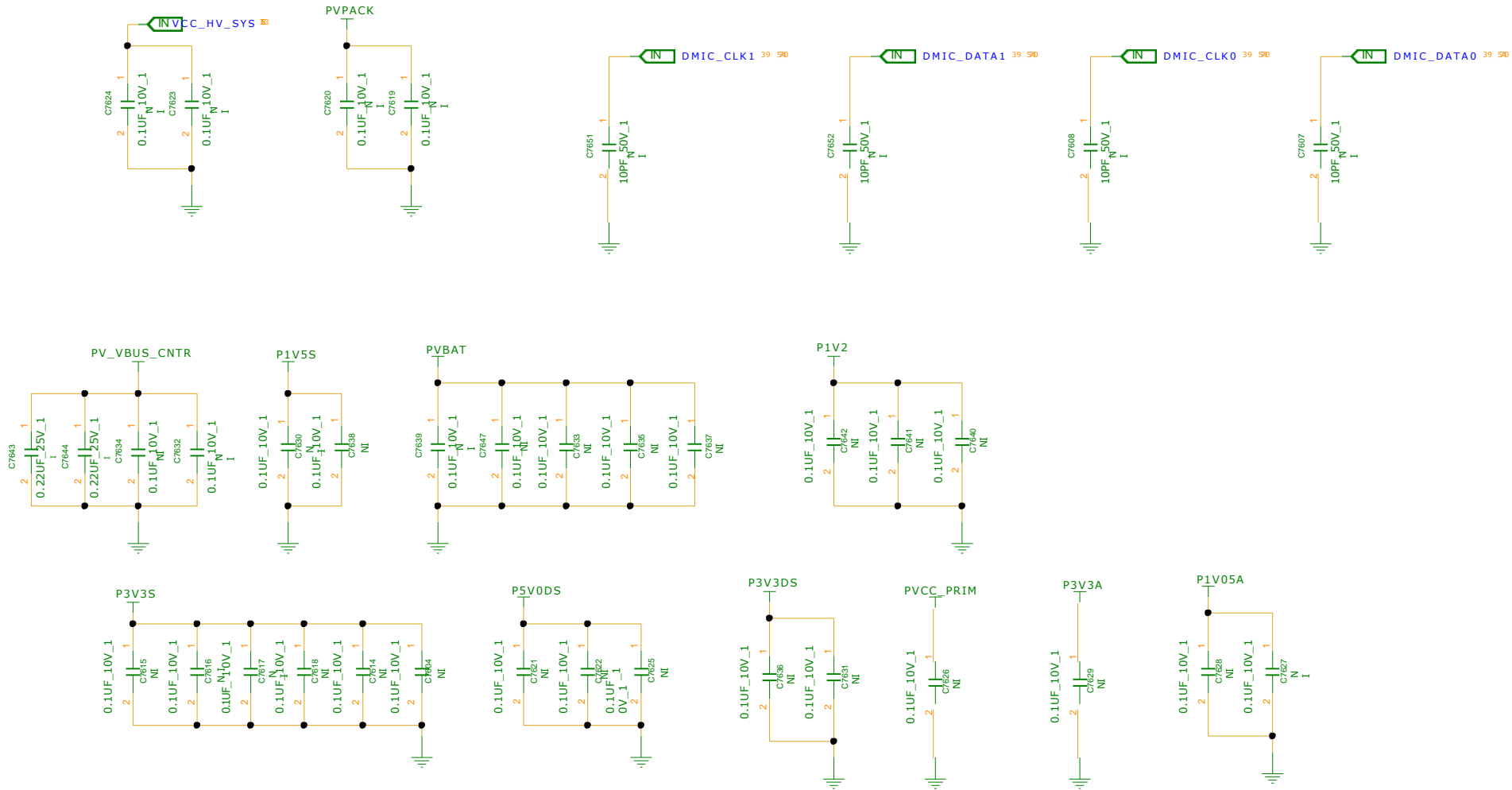
LOCATION NUMBER : 1800~1899

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	Eletro-XTech	PCB VER	XXX

INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310XXXX-0-0	X01
SHEET 74 of 86			

# EMI SOLUTION

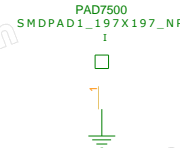
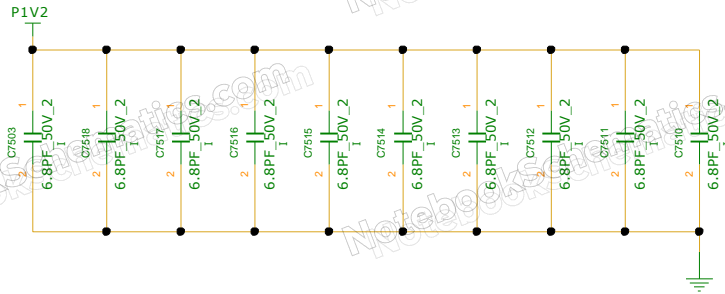


LOCATION NUMBER : 800C-8199

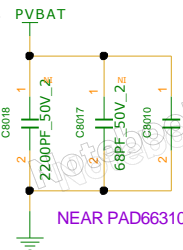
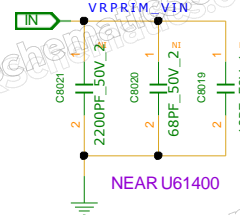
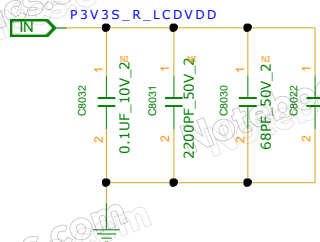
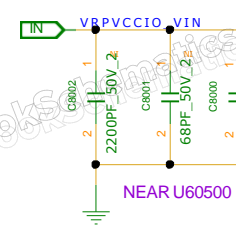
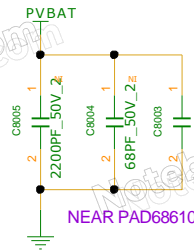
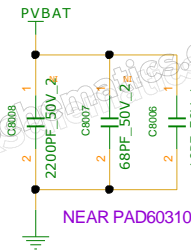
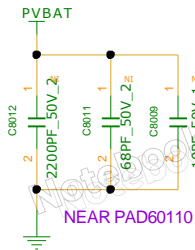
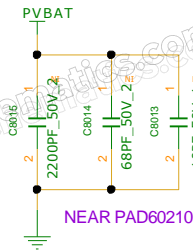
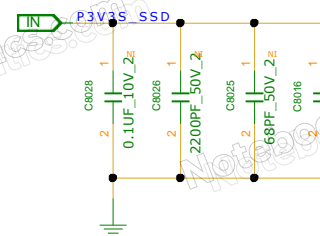
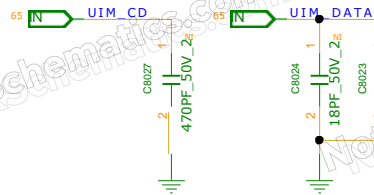
INVENTEC			
TITLE MODEL,PROJECT,FUNCTION EMI SOLUTION			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 76 of 86			

CHANGE by PCB P/N	XXX Eletro-XTech	DATE PCB VER	21-OCT-2002 XXX
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# RF SOLUTION



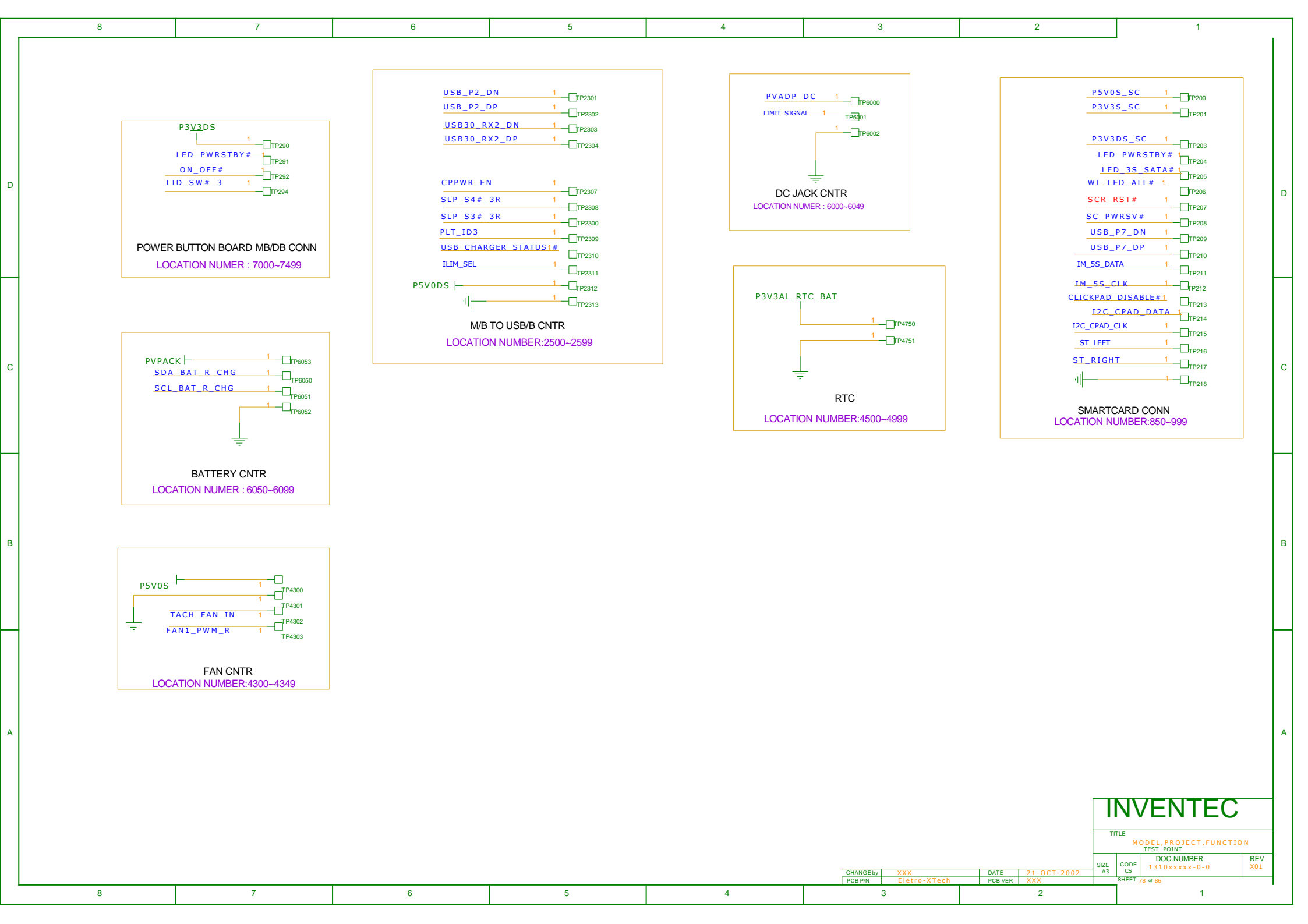
RF FOR WWAN SOLUTION  
NEED ADD UNDER THE MODULE

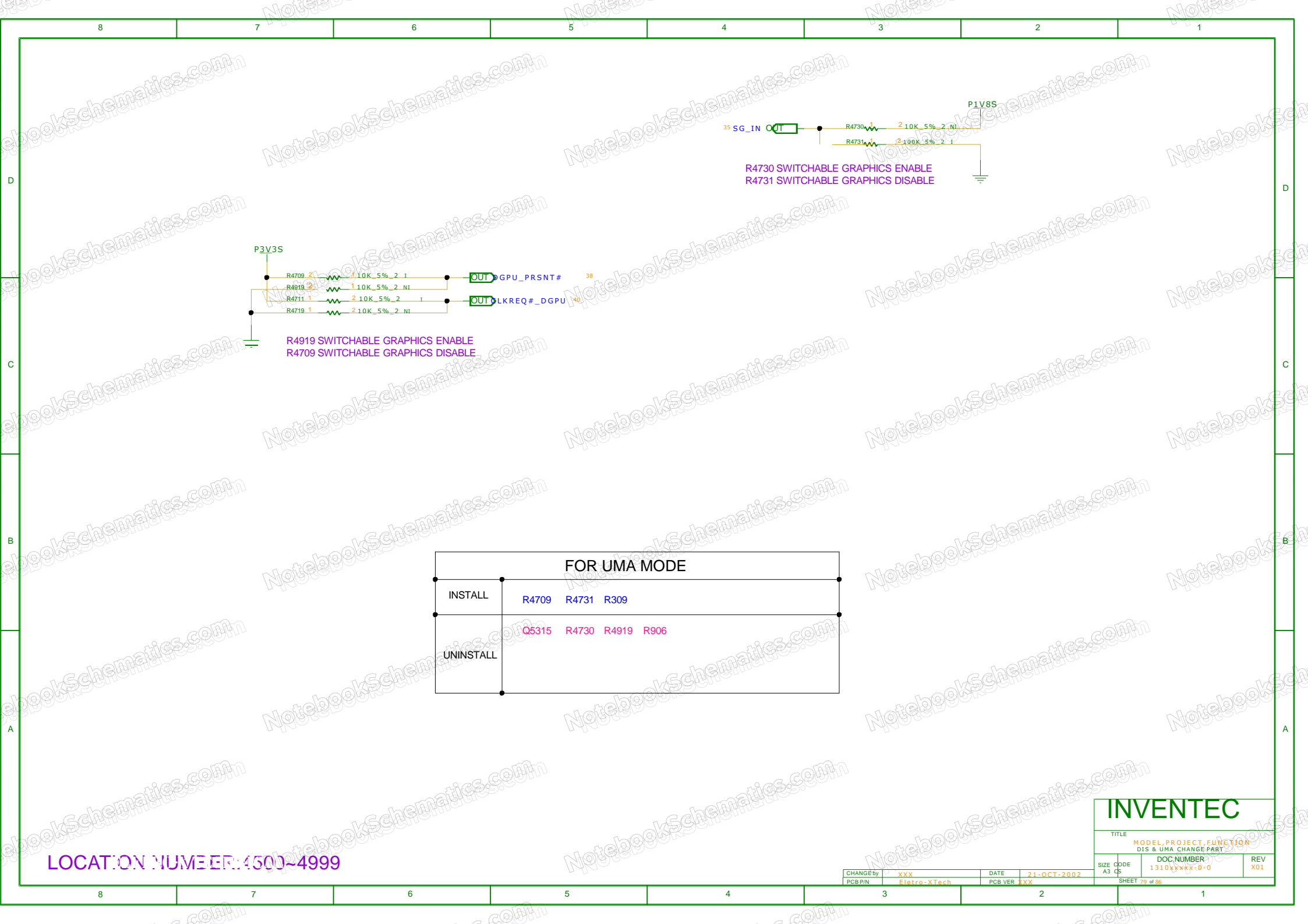


LOCATION NUMBER : 8000~8199

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	Eletra-XTech	PCB VER	XXX

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION RF SOLUTION			
DOC NUMBER 1310xxxx-0-0			REV X01
SIZE A3	CODE CS		
SHEET 77 of 86			





FOR UMA MODE				
INSTALL	R4709	R4731	R309	
UNINSTALL	Q5315	R4730	R4919	R906

LOCATION NUMBER: 4500~4999

INVENTEC

TITLE  
MODEL PROJECT FUNCTION  
DIS & UMA CHANGE PART

SIZE CODE  
A3 CS

DOC NUMBER  
1310xxxxx-0-0

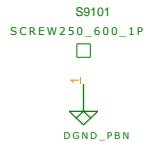
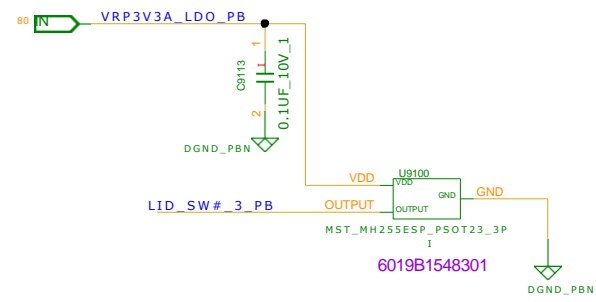
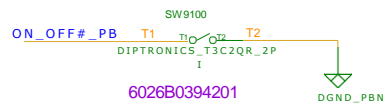
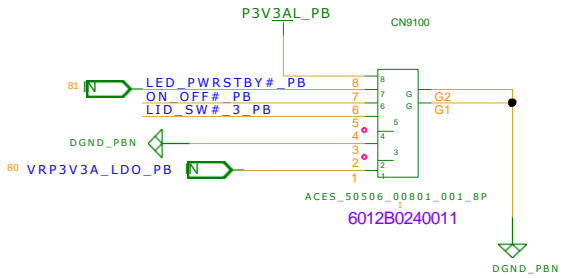
REV  
X01

CHANGE BY  
PCB P/N  
XXX  
Eletra-XTech

DATE  
PCB VER  
21-OCT-2002  
XXX

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FIX9104	FIX9105	FIX9102	FIX9103
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
FIX_MASK_0.8	FIX_MASK_0.8	FIX_MASK_0.8	FIX_MASK_0.8
FIX9106	FIX9107	FIX9100	FIX9101
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
FIX_MASK_0.8	FIX_MASK_0.8	FIX_MASK_0.8	FIX_MASK_0.8

# POWER SWITCH DAUGHTER BOARD

REFERENCE NUMBER:9100~9199

INVENTEC			
TITLE MODEL PROJECT FUNCTION POWER BUTTON BOARD			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 80 of 86			

CHANGE by PCB P/N	XXX Eletro-XTech	DATE PCB VER	21-OCT-2002 XXX
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POWER BUTTON BOARD LED

The schematic diagram illustrates the power button board LED circuit. It features a 100K resistor (R9102) connected to the P3V3AL\_PB pin. The circuit also includes a 0.5% 2N resistor (R9105) connected to the LED\_PWRSTBY# PB pin. A 0.5% 2N resistor (R9101) is connected to the P3V3AL\_PB pin. The circuit is powered by a 30.5% 2N resistor (R9100). The output is labeled OUT.

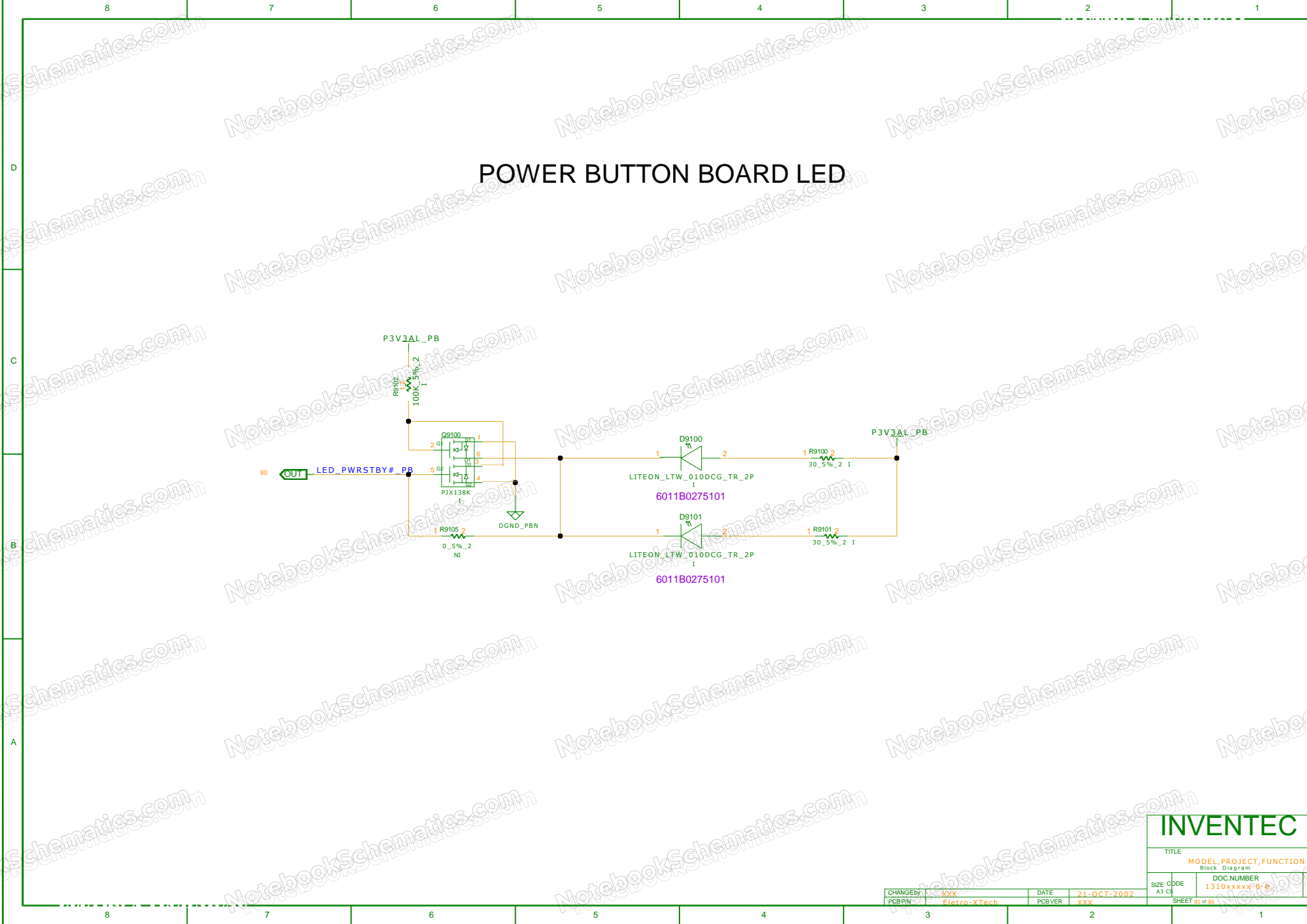
INVENTEC

TITLE MODEL PROJECT FUNCTION  
Block Diagram

SIZE CODE A3 C4  
DOC NUMBER 1310xxxx-0-0  
SHEET 61 of 86

CHANGED BY XXX  
PCB P/N XXX

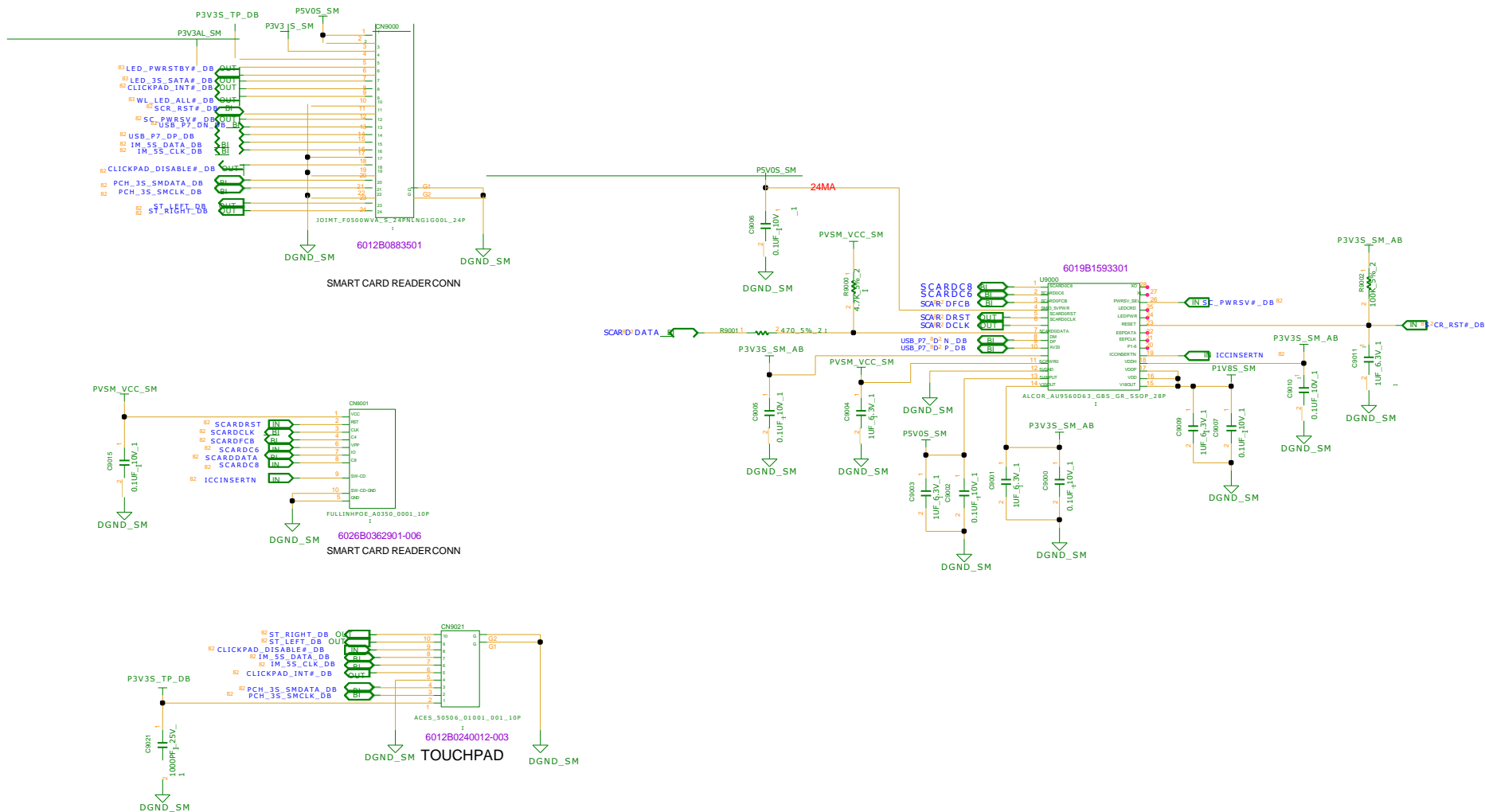
DATE 21-OCT-2002  
PCB VER XXX



CHANGEby	XXX
PCBP/N	Eletro-XTech

F  
E  
D  
C  
B  
A

F  
E  
D  
C  
B  
A



FIX9000	FIX9001	FIX9002
FIX_MASK_0.8	FIX_MASK_0.8	FIX_MASK_0.8
FIX9003	FIX9004	FIX9005
FIX_MASK_0.8	FIX_MASK_0.8	FIX_MASK_0.8

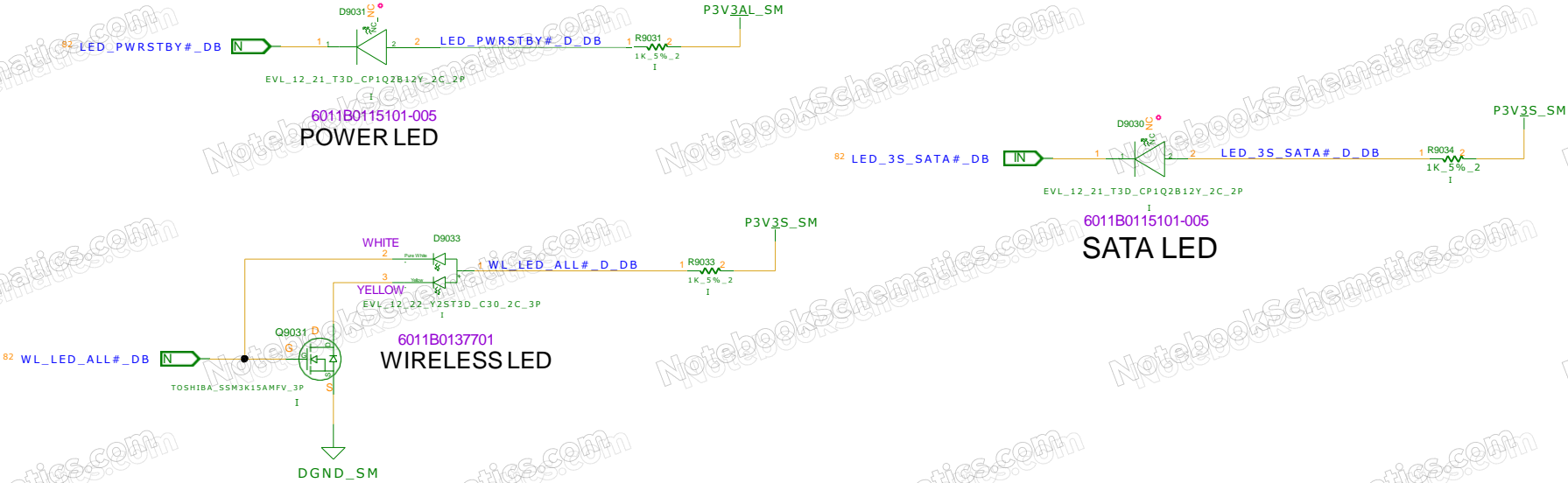
# SMART CARD DOUGHTER BOARD

LOCATION NUMBER:9100~9199

INVENTEC			
MODEL	PROJECT,FUNCTION	LOC NUMBER	REV
SMART CARD DB		1310xxxx-0-0	X01
SIZE	CODE	SHEET	of 40
A3	CS		

CHANGEN	xxx	DATE	21-OCT-2002
PCBPN	FilePro_XTech	PCBVER	xxx

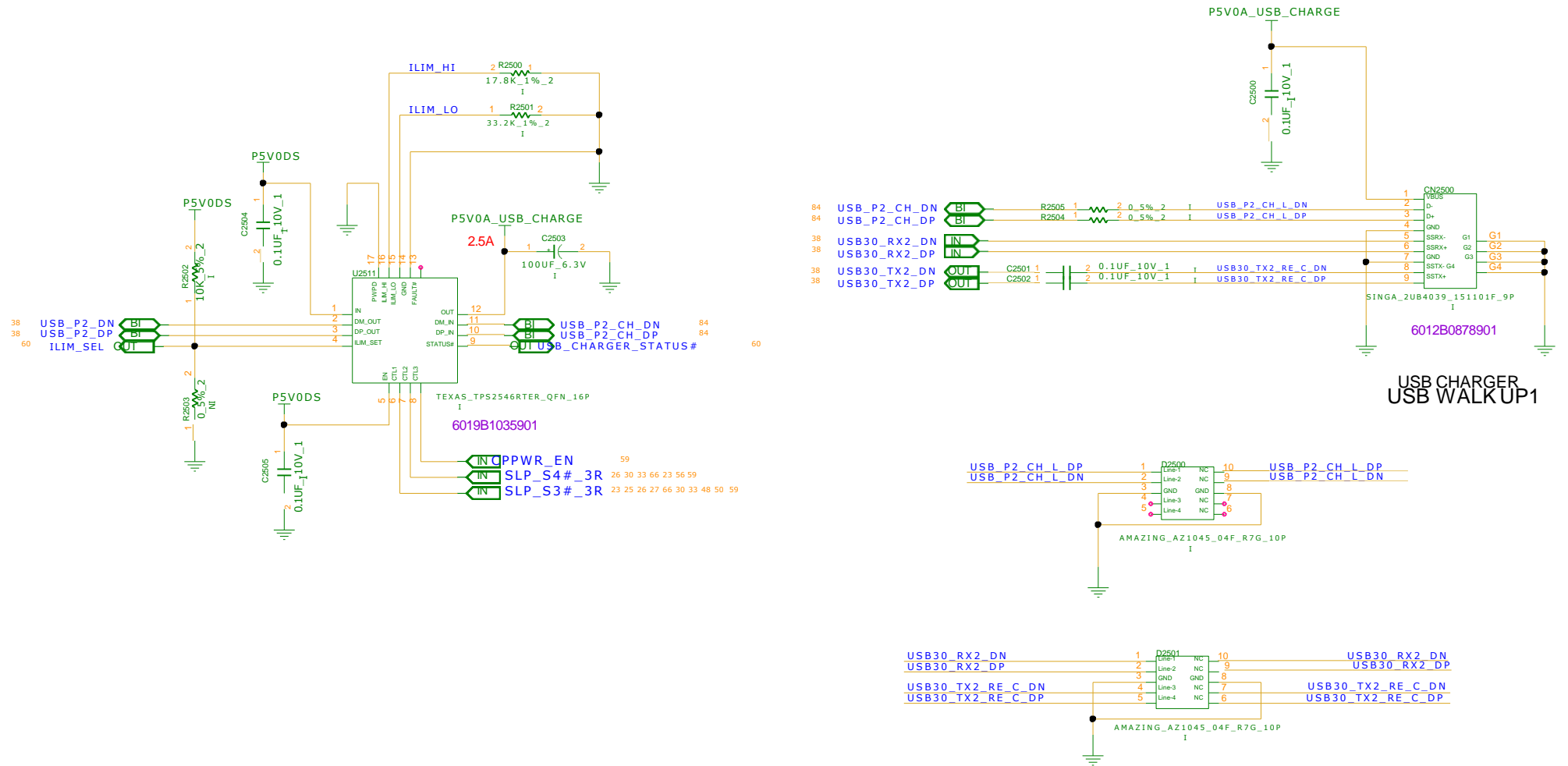
# SMART CARD D/B LED



LOCATION NUMBER: 9100~9199

INVENTEC				
TITLE MODEL, PROJECT, FUNCTION SMART CARD DB, LED				
SIZE A3		CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
CHANGE by	XXX	DATE	21-OCT-2002	
PCB P/N	Eletro-XTech	PCB VER	XXX	
SHEET 83 of 86				

## USB\_TYPEA\_CHARGING PORT



LOCATION NUMBER: 9200~9299

# INVENTEC

TITLE	MODEL,PROJECT,FUNCTION
	USB CHARGER PORT

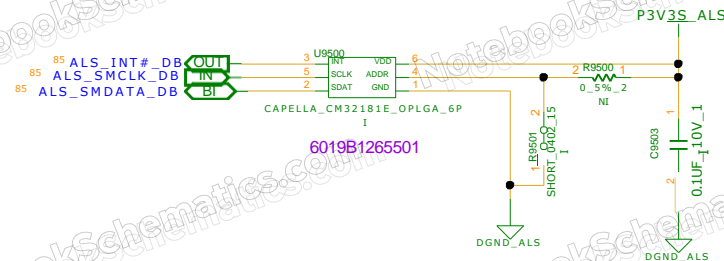
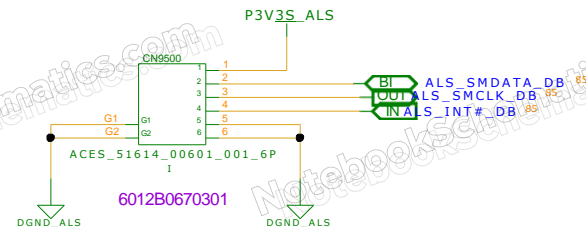
DOC.NUMBER		
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SIZE	CODE	1310xxxxx-0-0
A3	CS	

REV  
X01

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	Eletro-XTech	PCB VER	XXX

SHEET 84 of 86



## AMBIENT LIGHT SENSOR

ADDR IS HIGH, SELECT ADDRESS 0X48  
ADDR IS LOW, SELECT ADDRESS 0X10

LOCATION NUMBER: 9300~9349

FIX3850  
□  
● FIX\_MASK\_0.8

FIX3851  
□  
● FIX\_MASK\_0.8

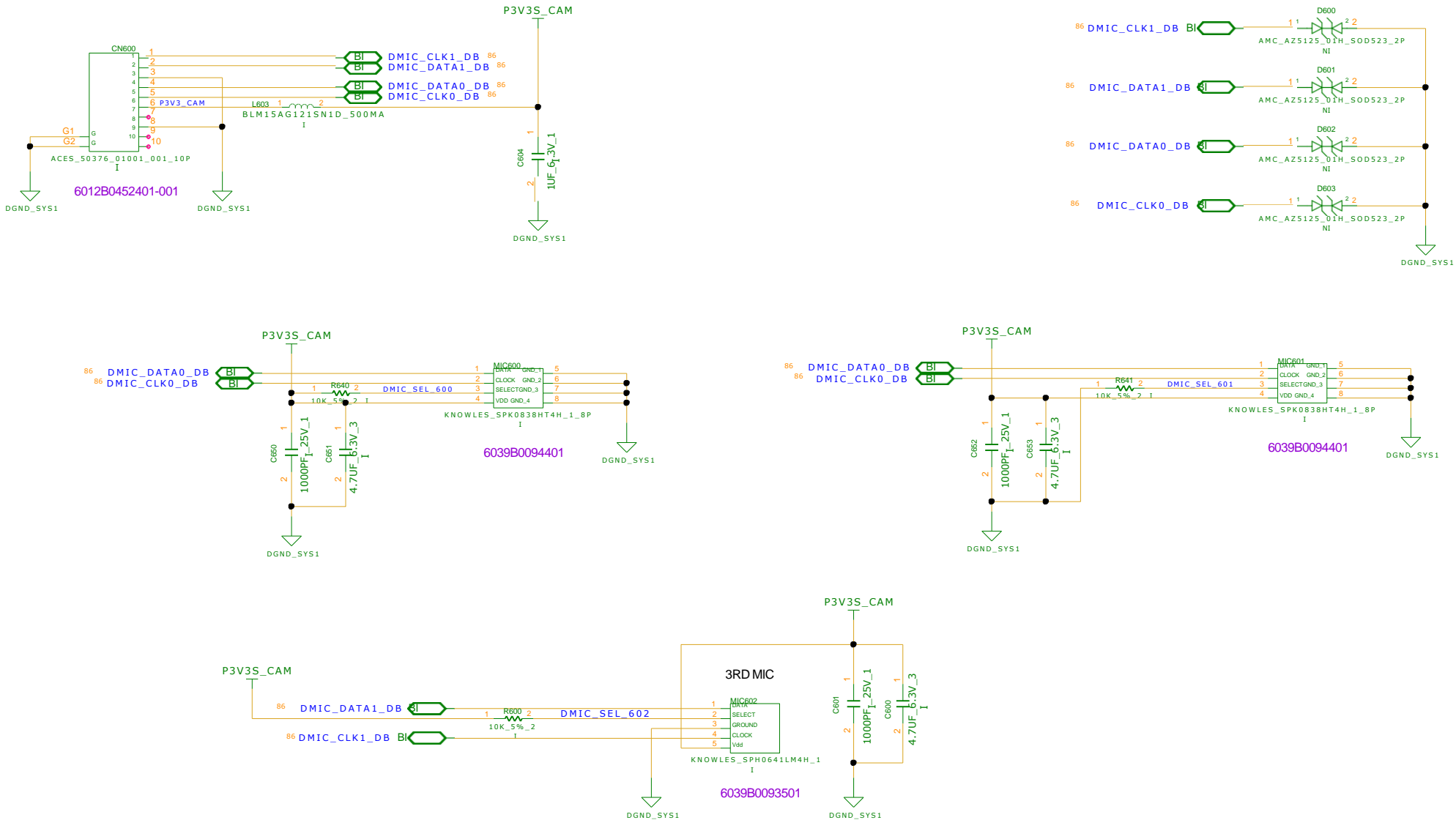
# INVENTEC

TITLE MODEL PROJECT FUNCTION  
ALS D/B

SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0.0	REV X01
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CHANGE by PCB P/N	XXX Eletra-XTech	DATE PCB VER	21-OCT-2002 XXX
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SHEET 85 of 86



# MIC DOUGHTER BOARD

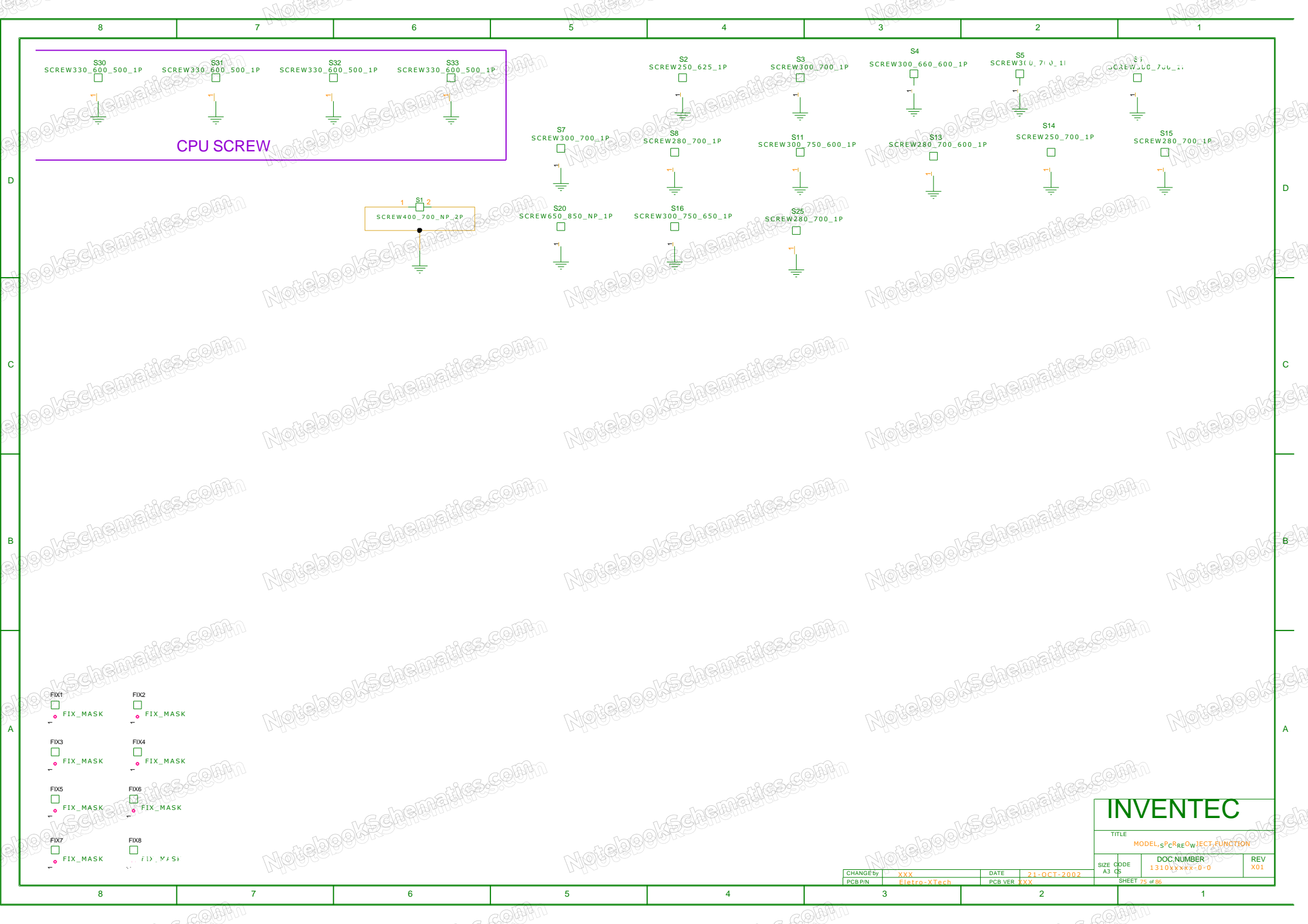
LOCATION NUMBER: 9350-9399

- FIX600
- FIX601
- FIX\_MASK\_0.8
- FIX\_MASK\_0.8

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
MIC BOARD			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 86 of 86			

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	Eletro-XTech	PCB VER	XXX





CPU SCREW

S1  
SCREW400\_700\_NP\_2P

- FIX1  
FIX\_MASK
- FIX2  
FIX\_MASK
- FIX3  
FIX\_MASK
- FIX4  
FIX\_MASK
- FIX5  
FIX\_MASK
- FIX6  
FIX\_MASK
- FIX7  
FIX\_MASK
- FIX8  
FIX\_MASK

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	Eletro-XTech	PCB VER	XXX

TITLE		MODEL_sPcRROWJECT_FUNCTION	
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 75 of 86			